

Figure 1A

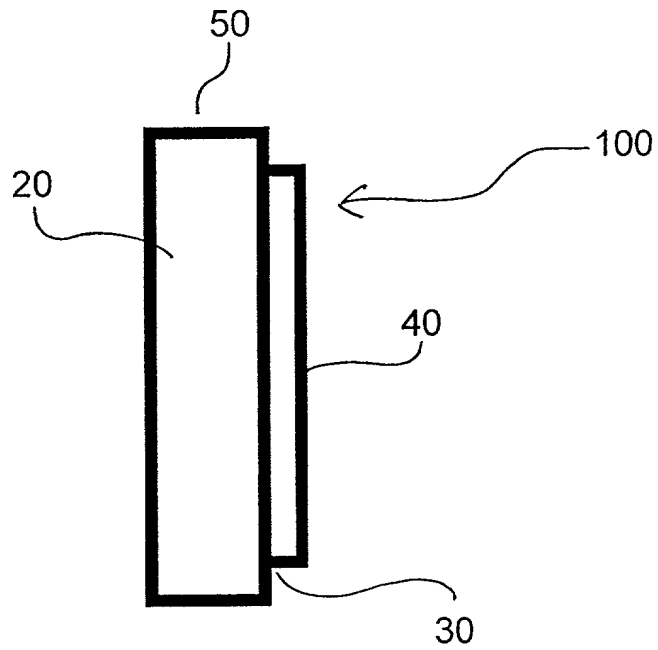


Figure 1B

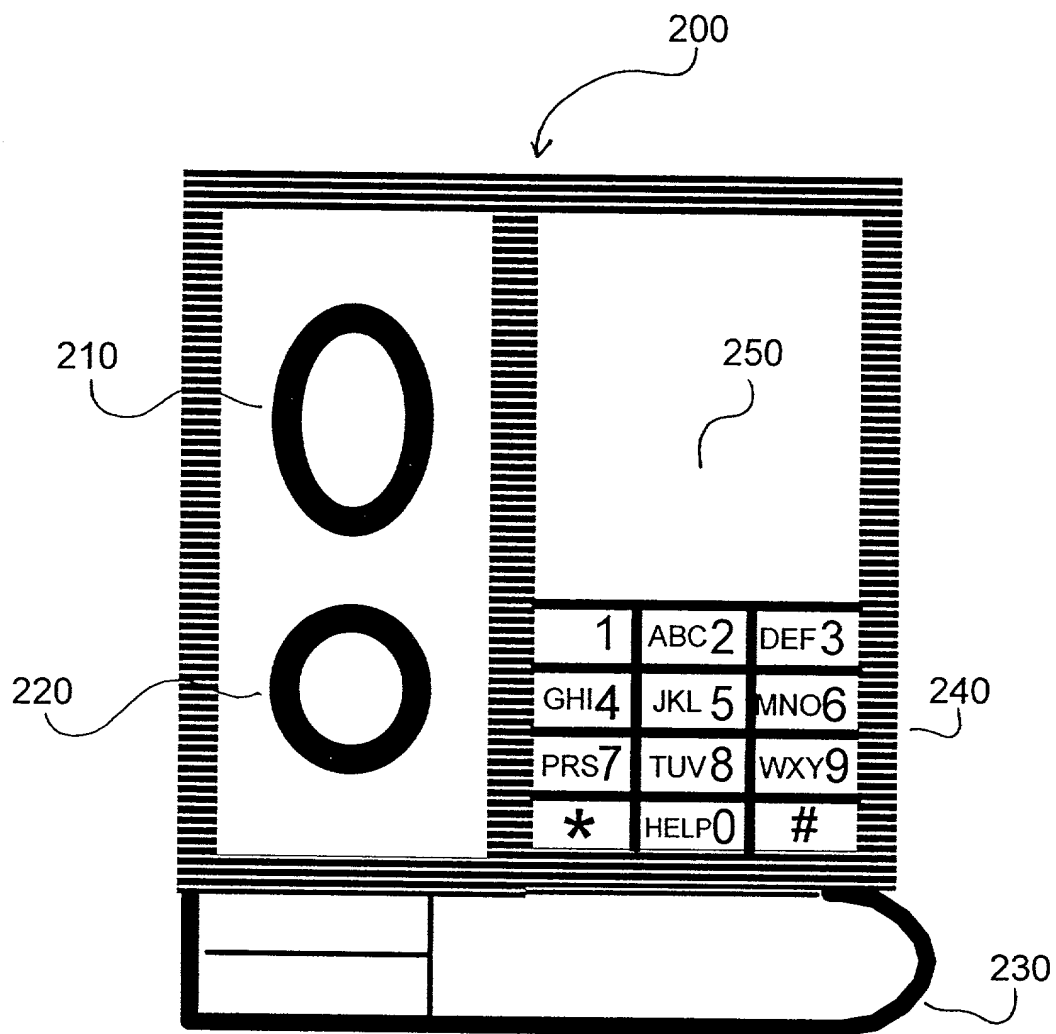


Figure 2

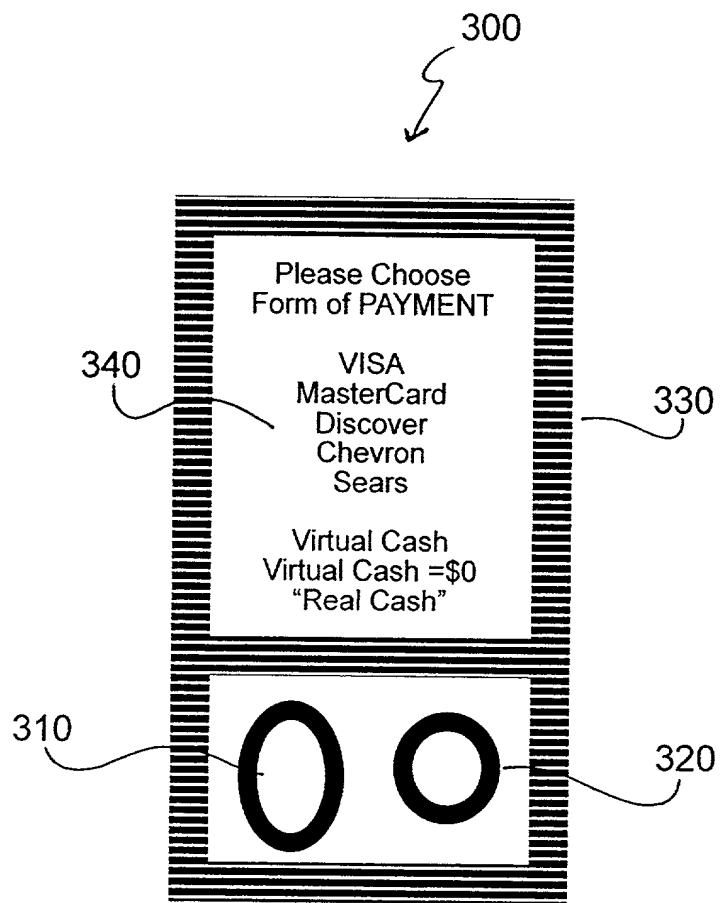


Figure 3.

Registration

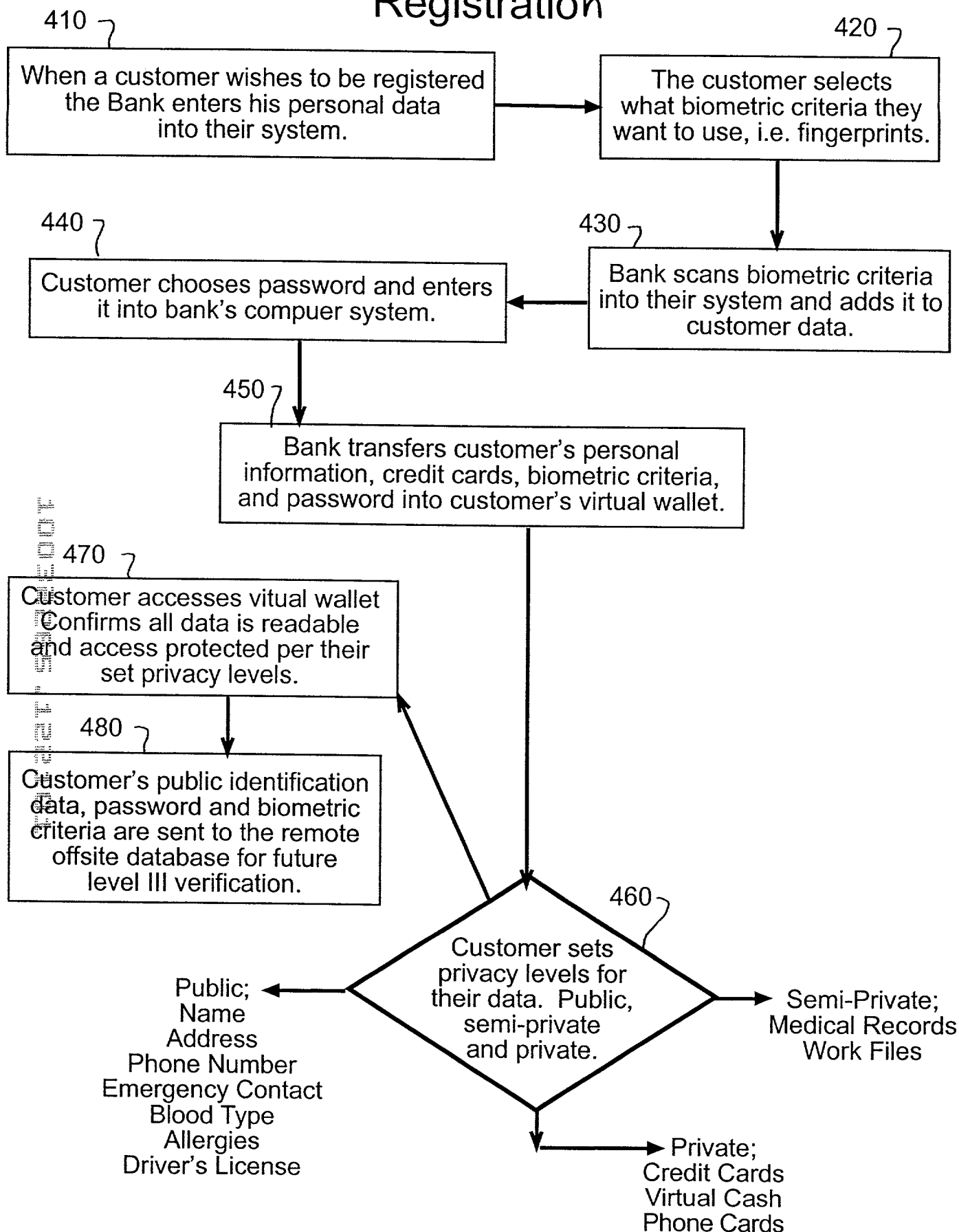


Figure 4

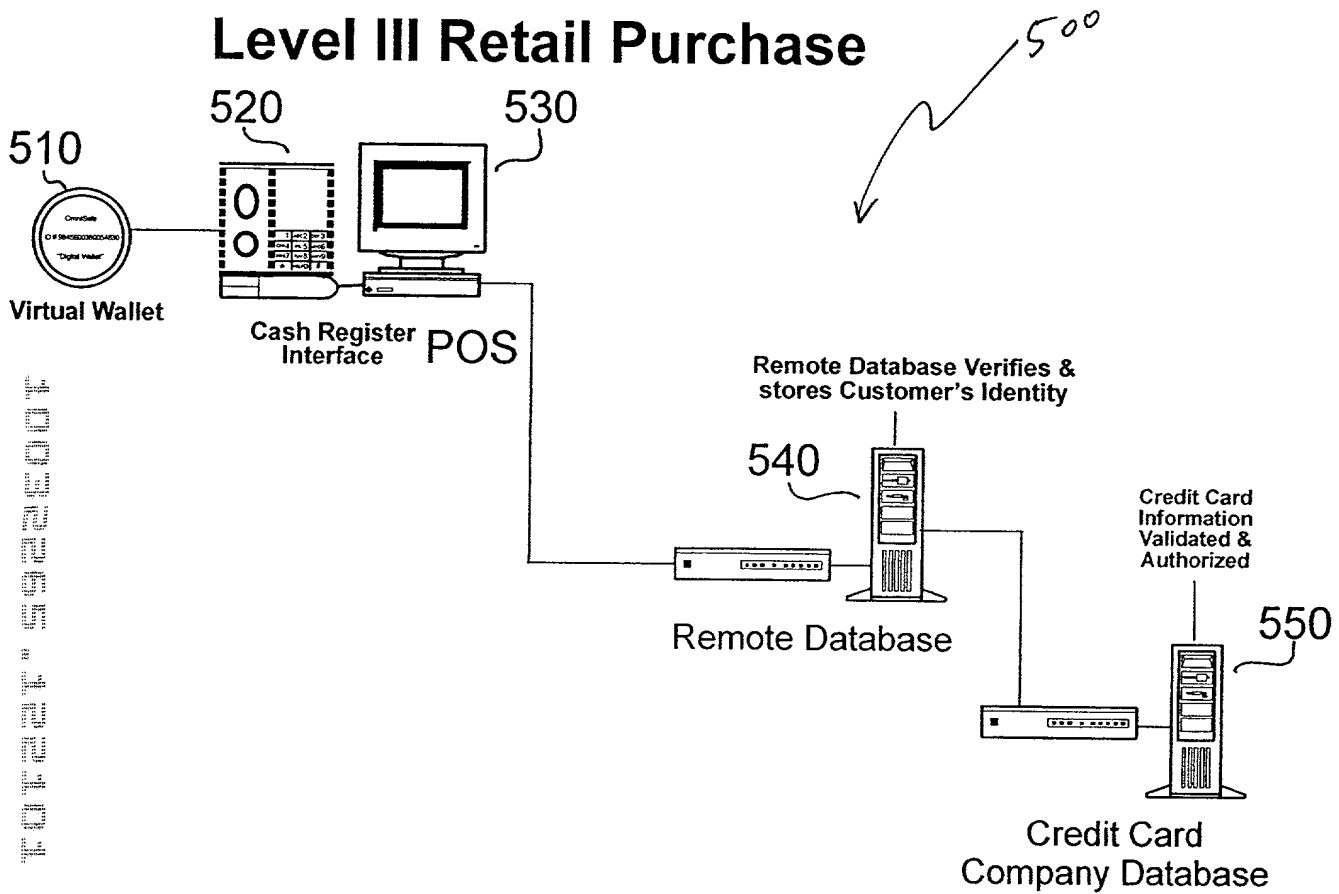


Figure 5

Home Purchase

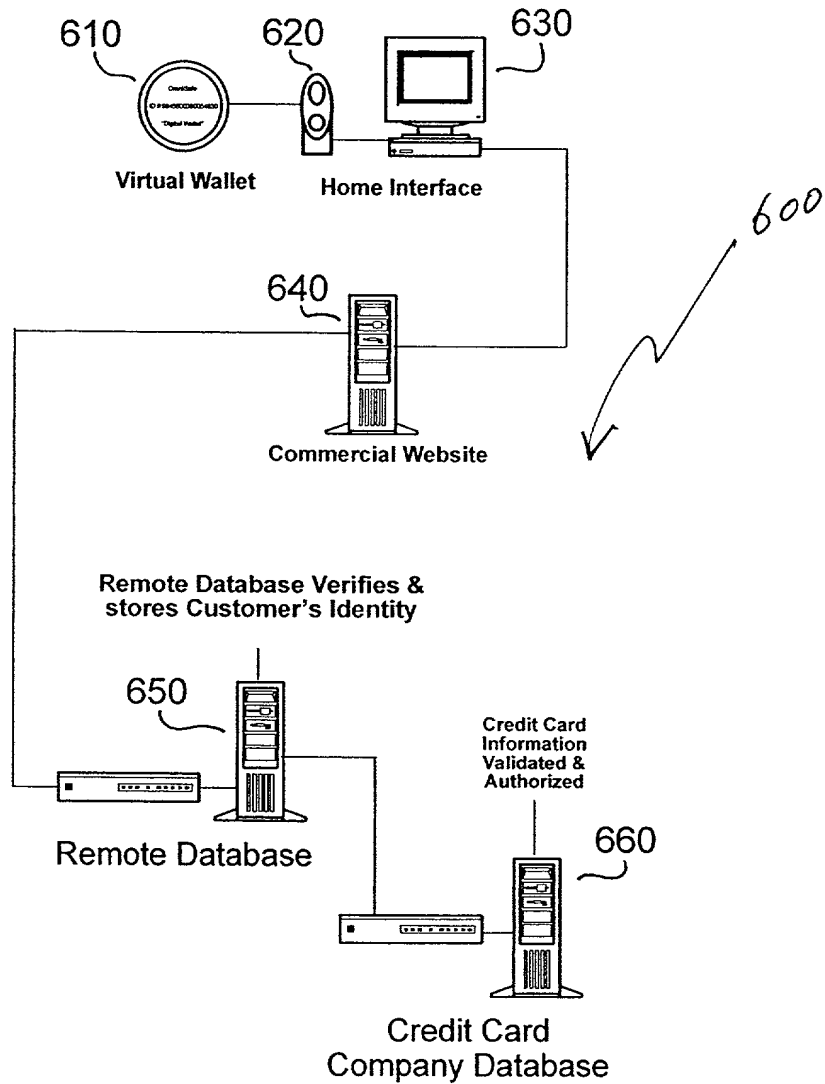


Figure 6

Security Level Logic Diagram

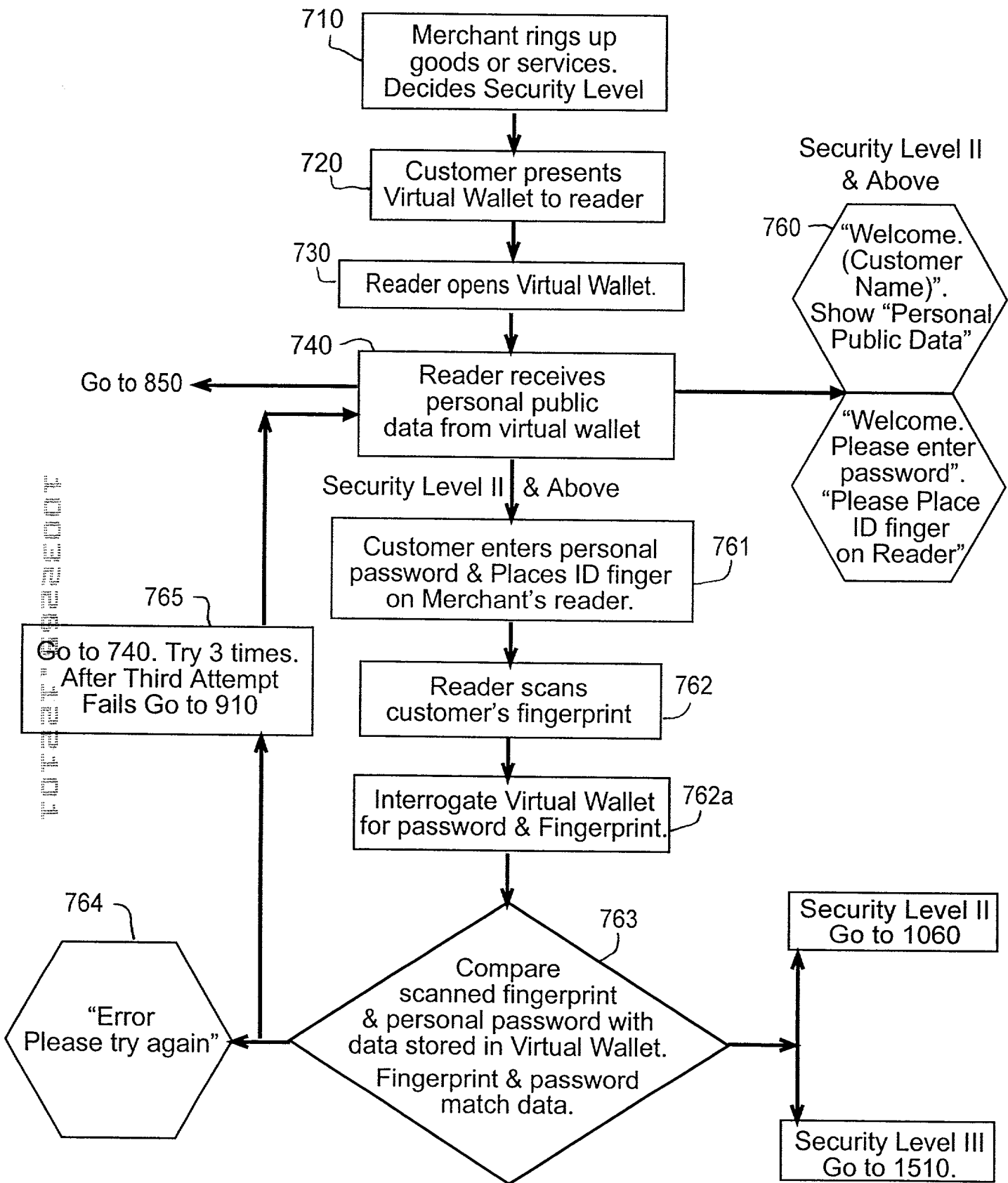


Figure 7

Security Level Logic Diagram

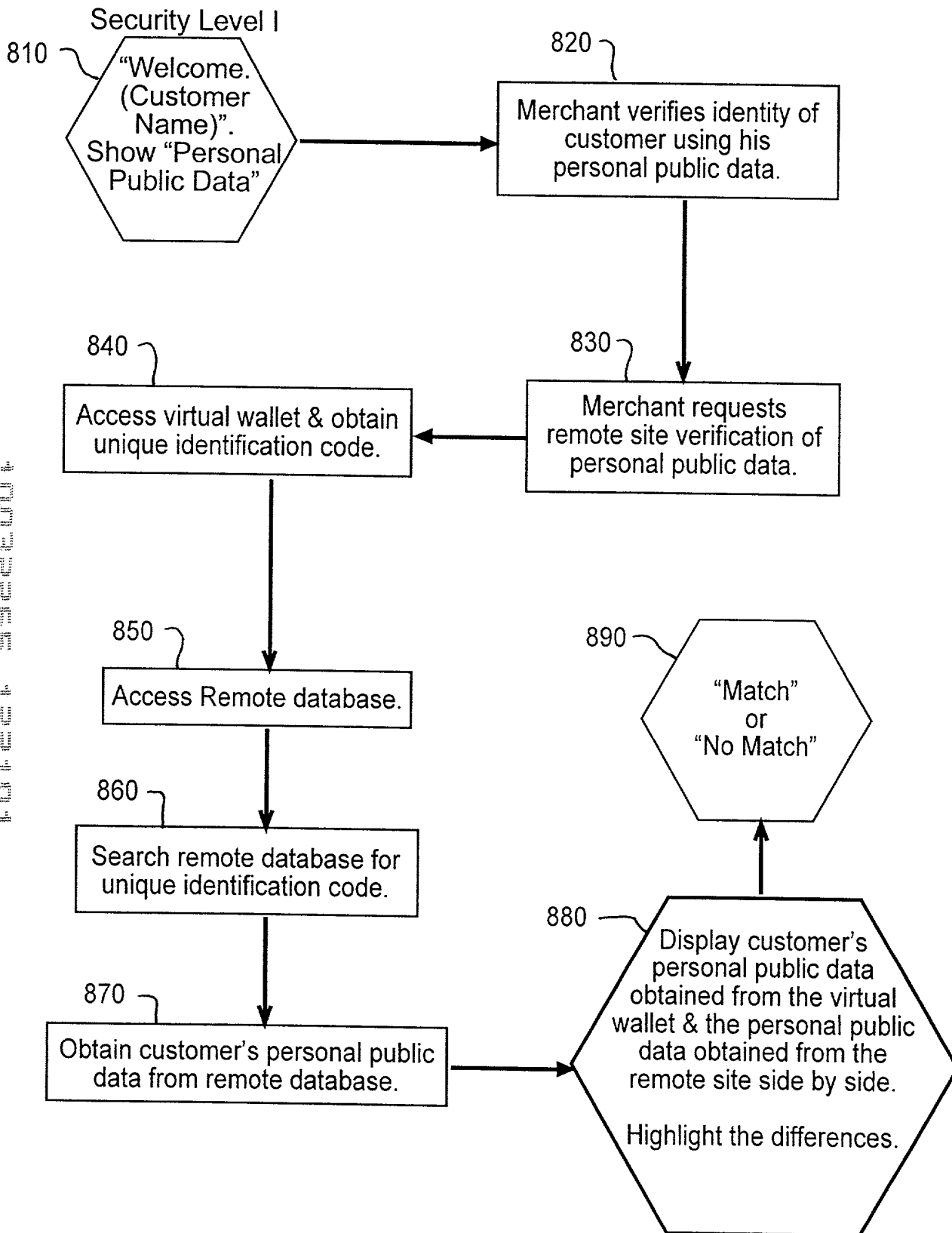


Figure 8

Security Level Logic Diagram

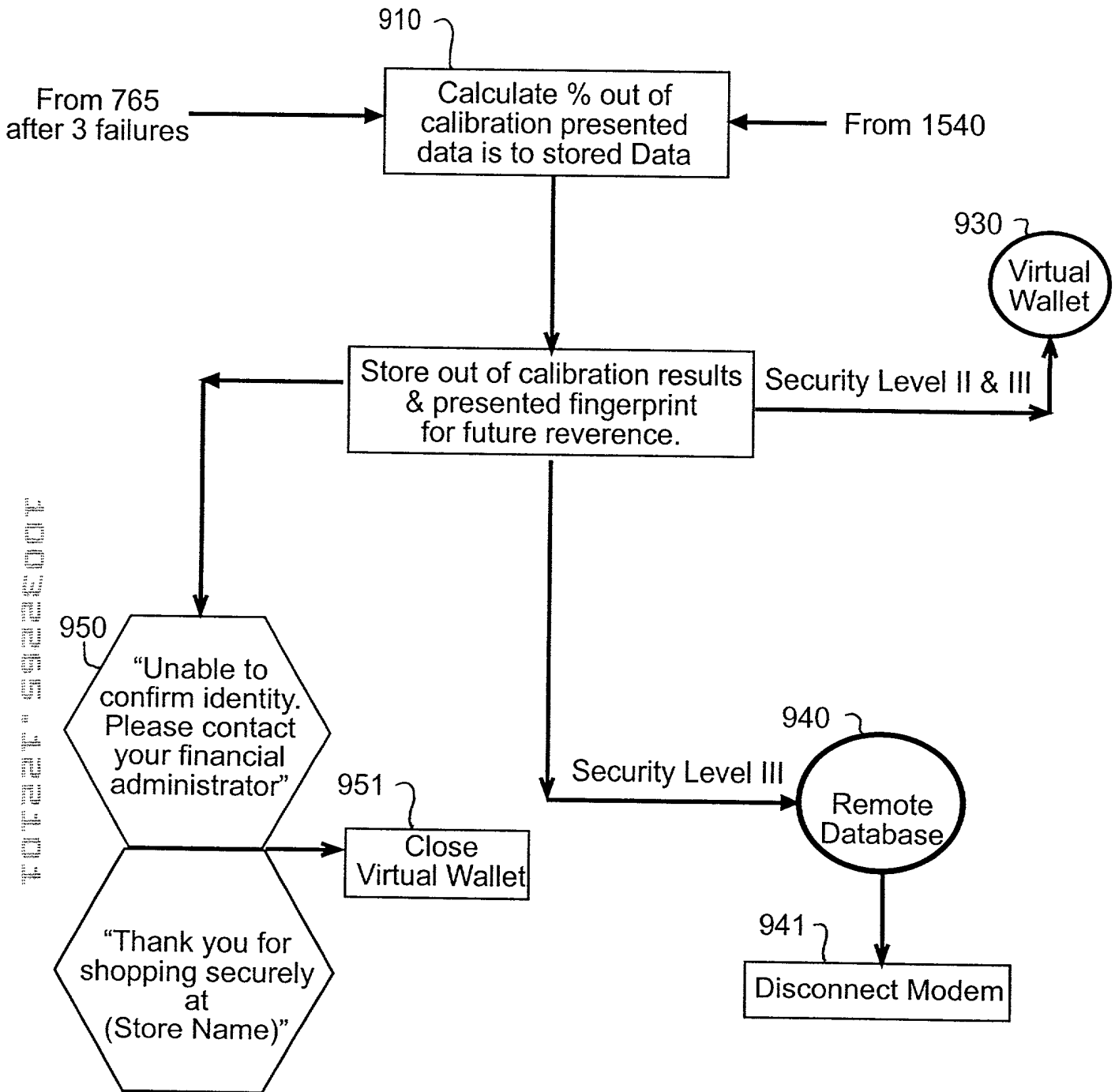


Figure 9

Security Level Logic Diagram

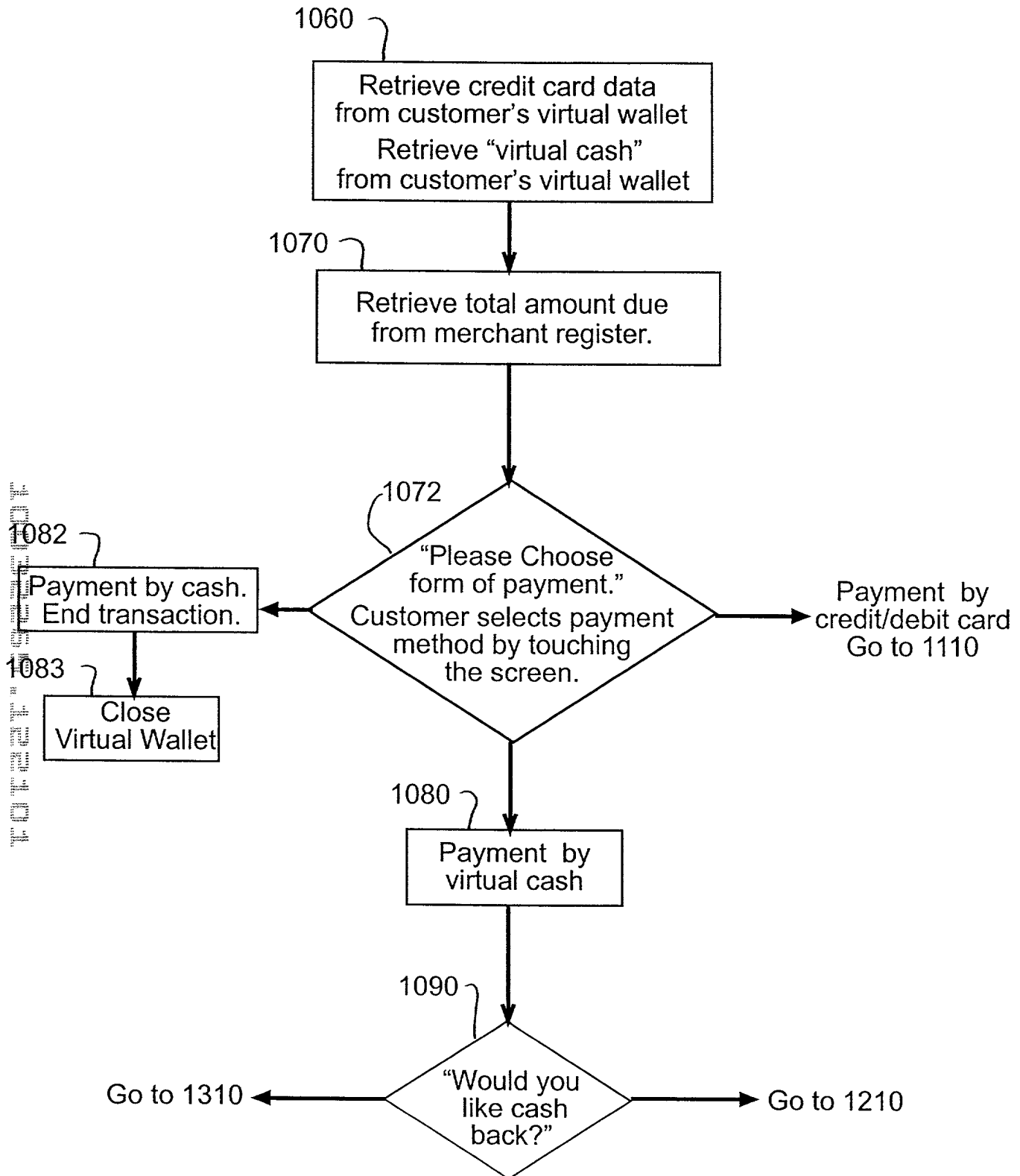


Figure 10

Security Level Logic Diagram

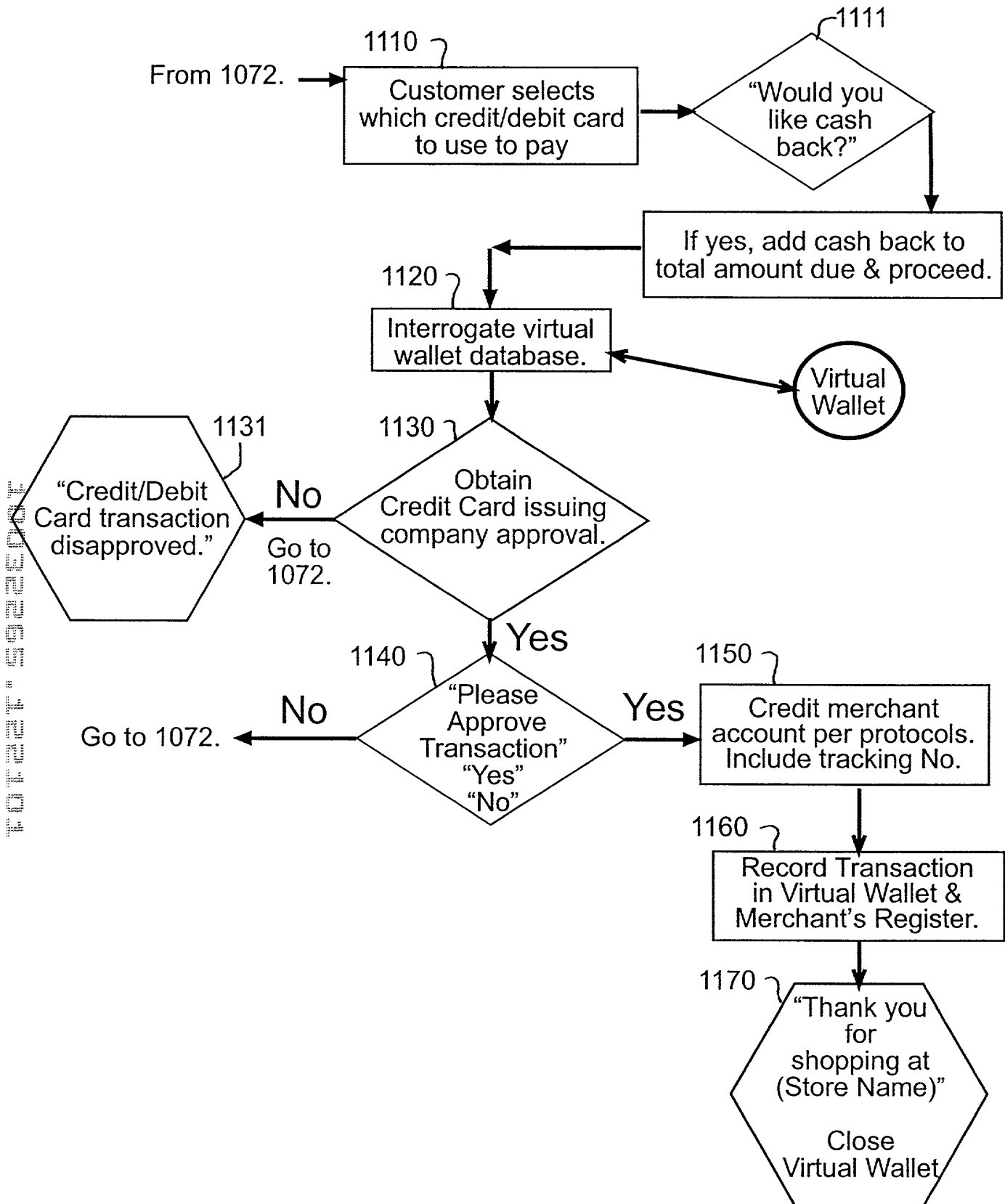


Figure 11

Security Level Logic Diagram

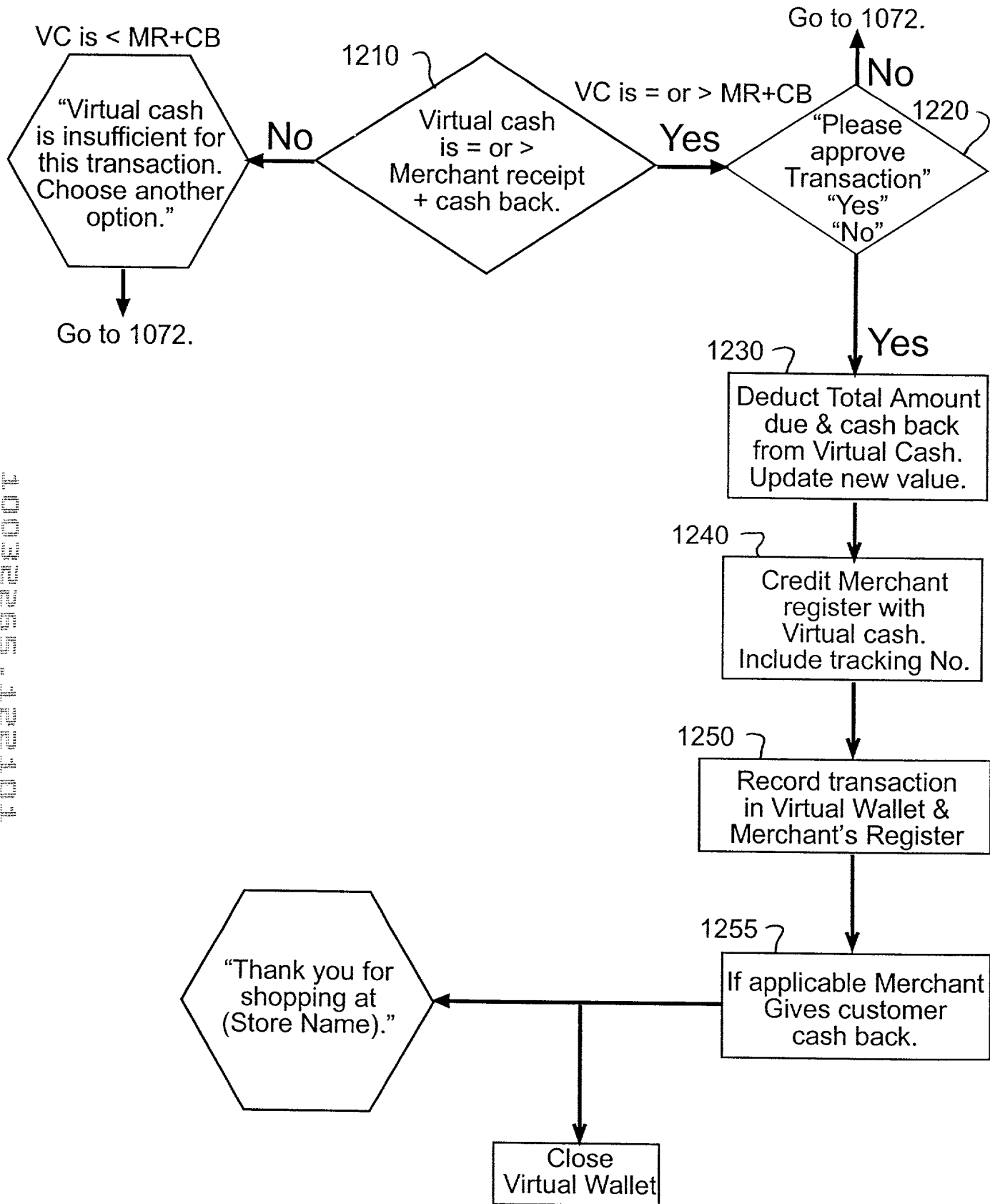


Figure 12

Security Level Logic Diagram

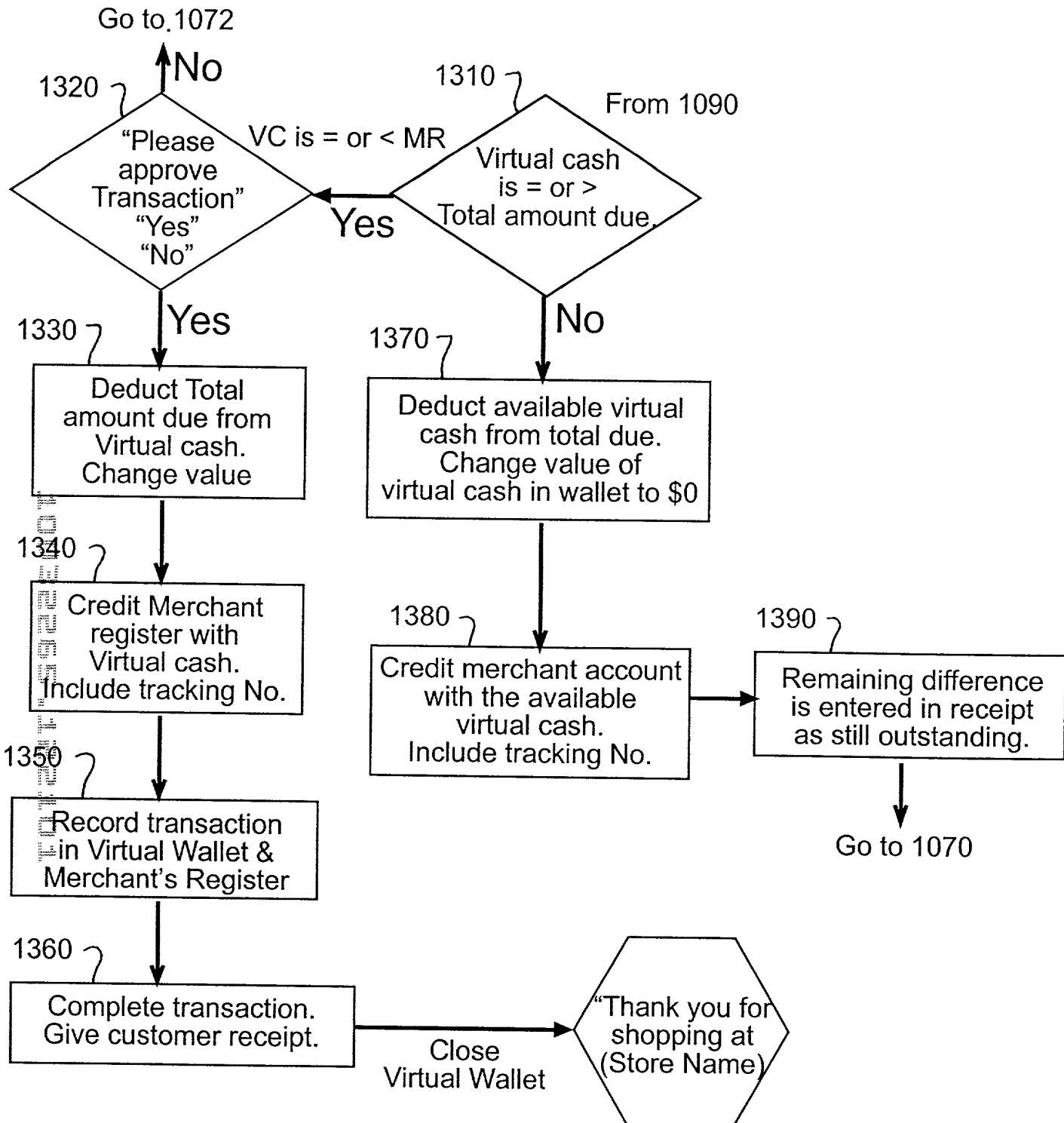


Figure 13

Security Level Logic Diagram

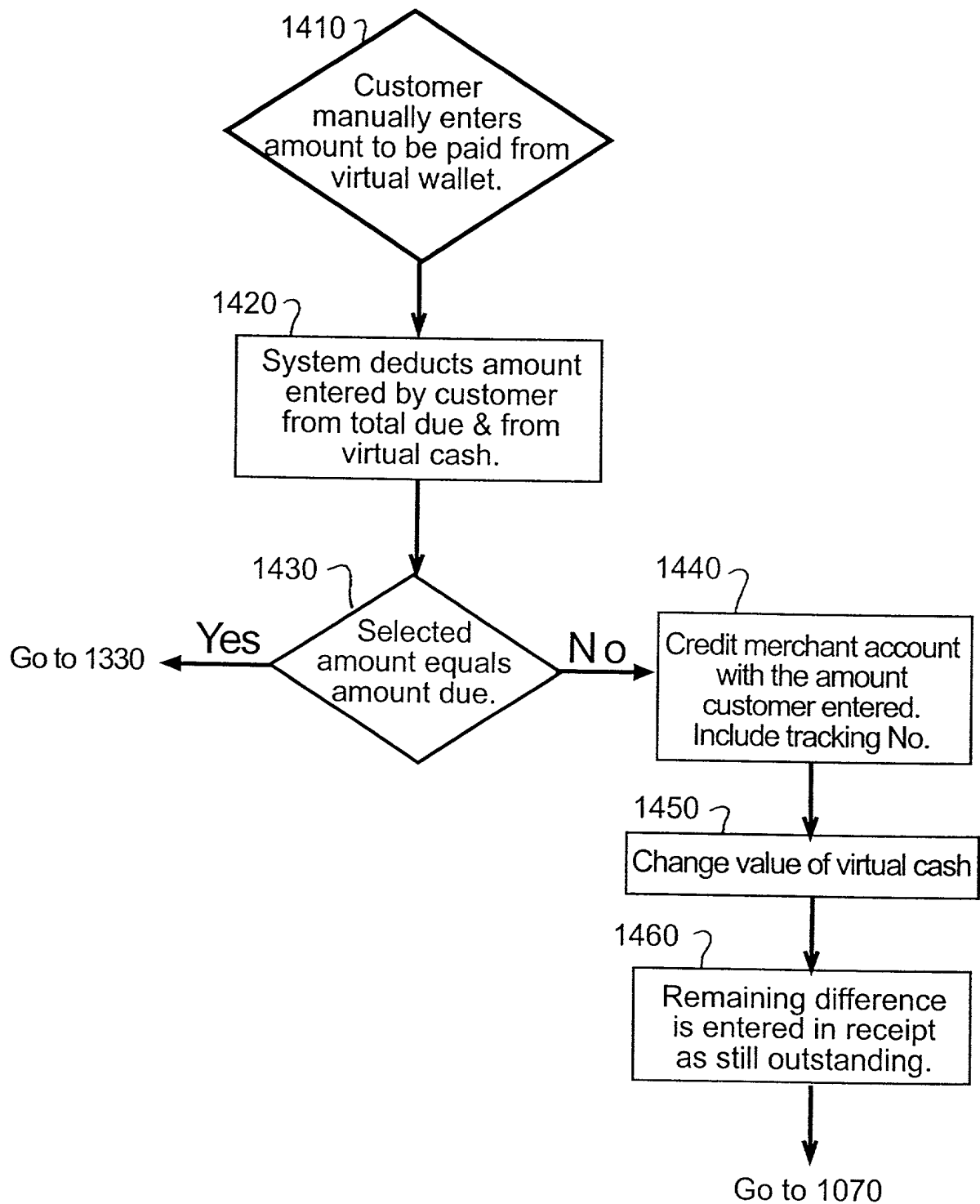


Figure 14

Security Level Logic Diagram

Security Level III Addendum

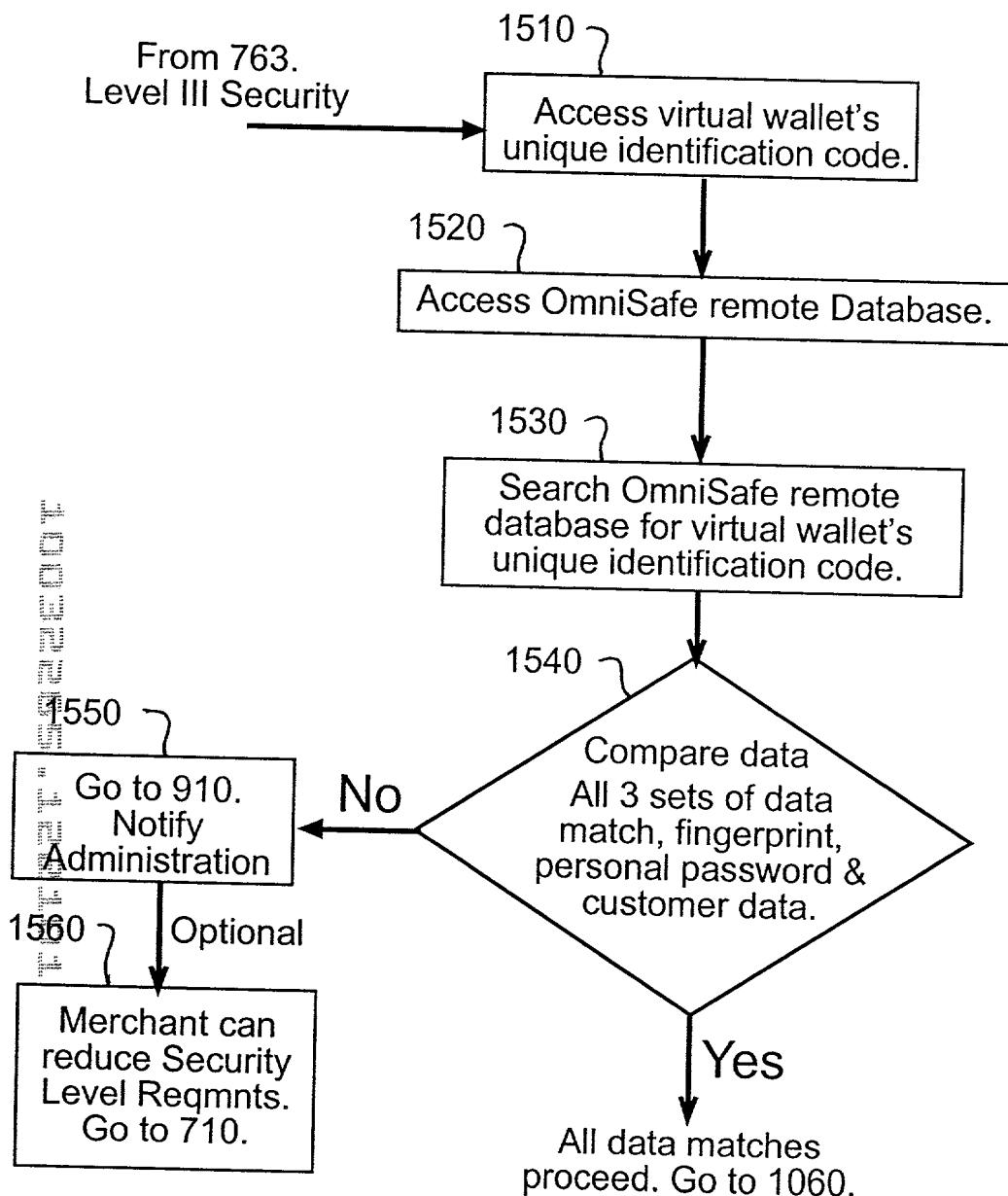


Figure 15

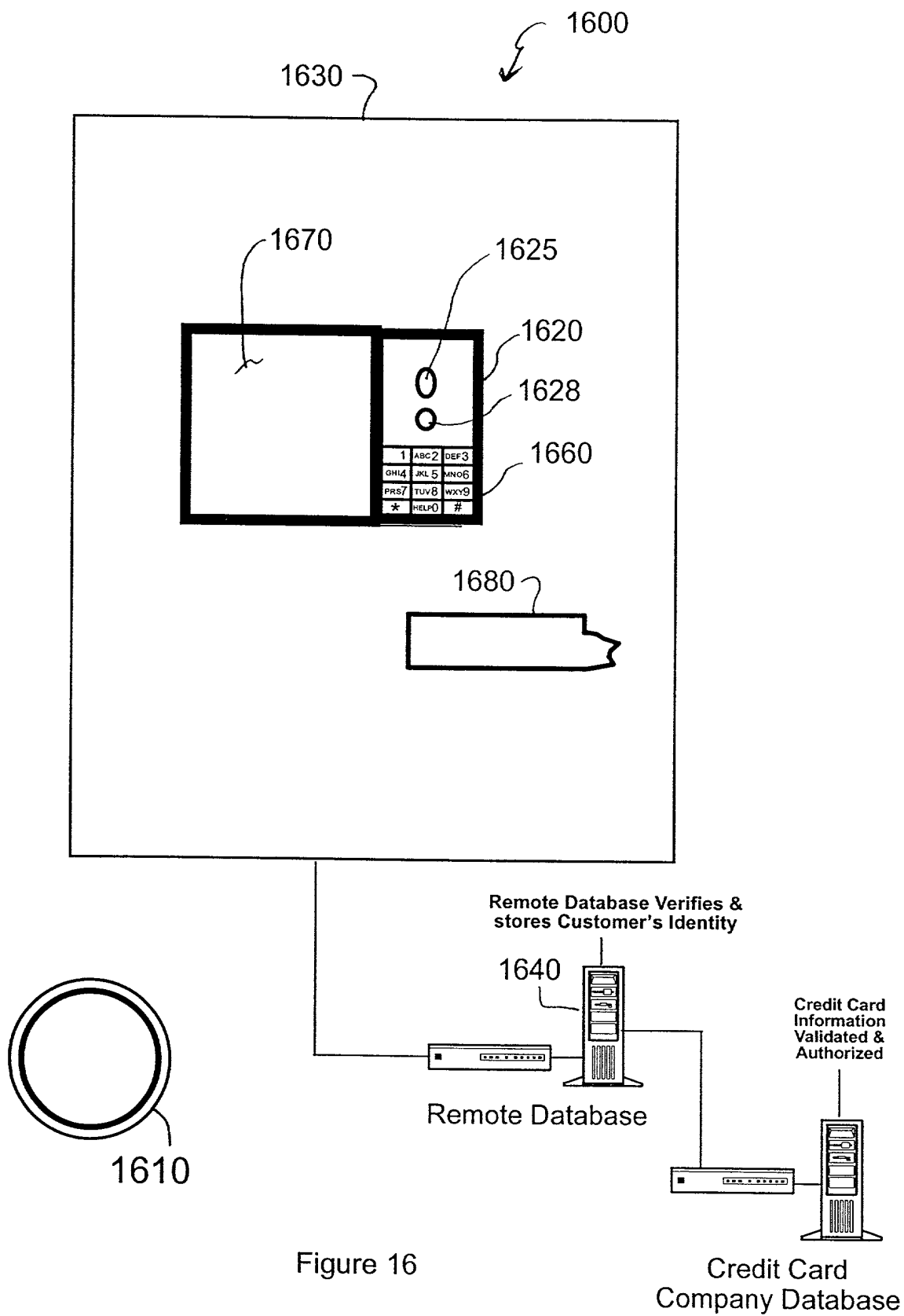


Figure 16

Security Level Logic Diagram

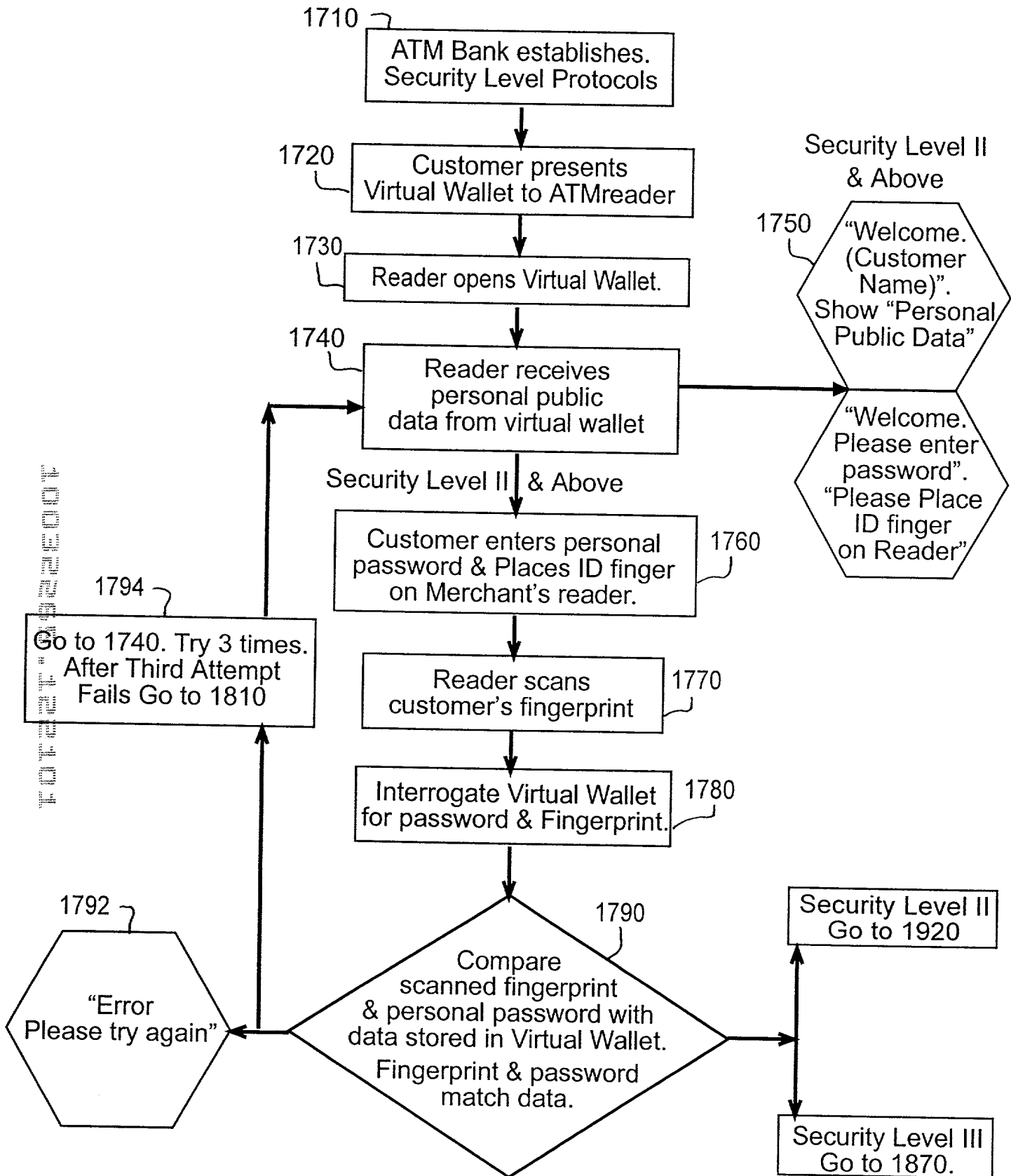


Figure 17

Security Level Logic Diagram

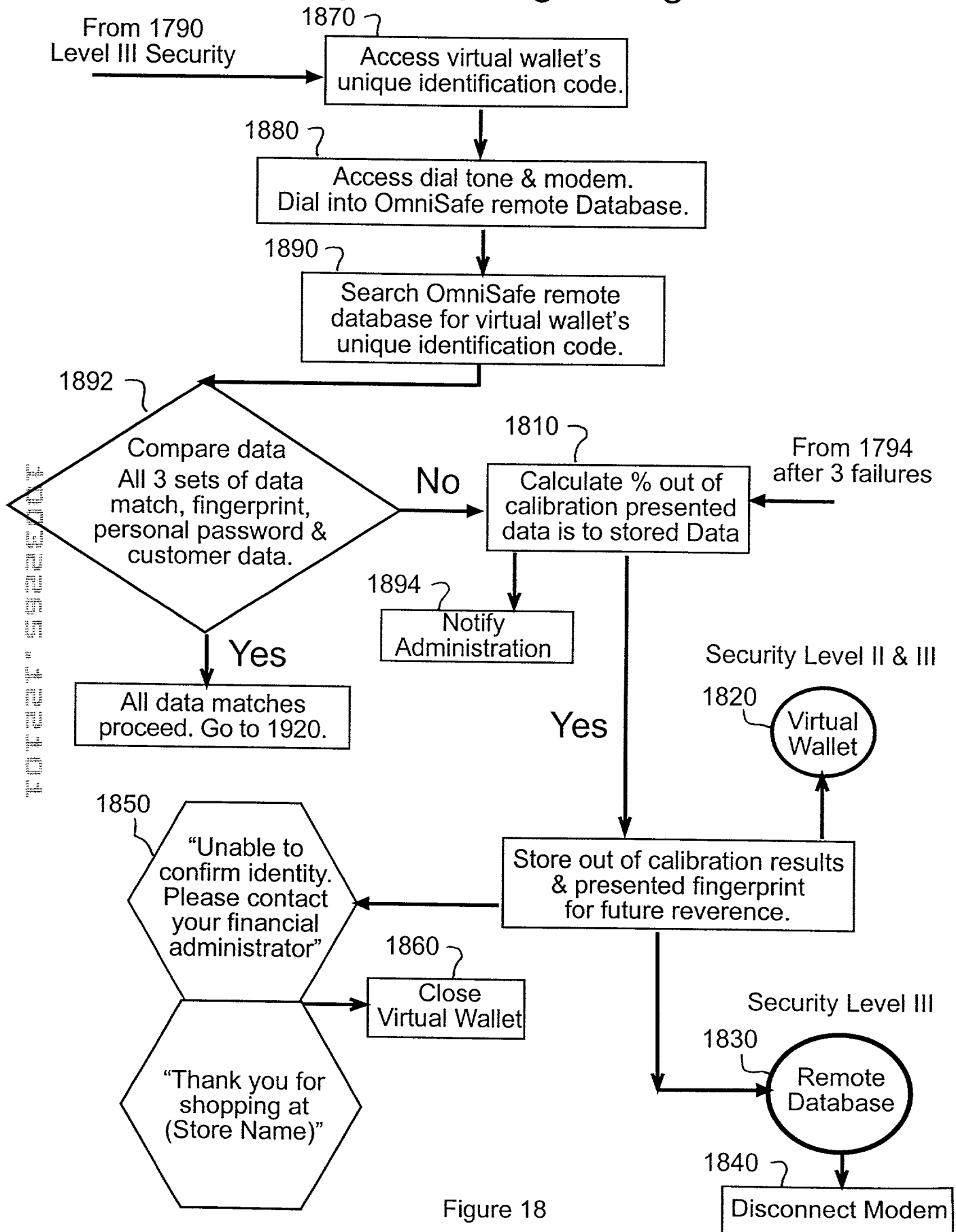


Figure 18

Security Level Logic Diagram

Note; ATM bank
can raise or lower
Security Level based on
transaction selected

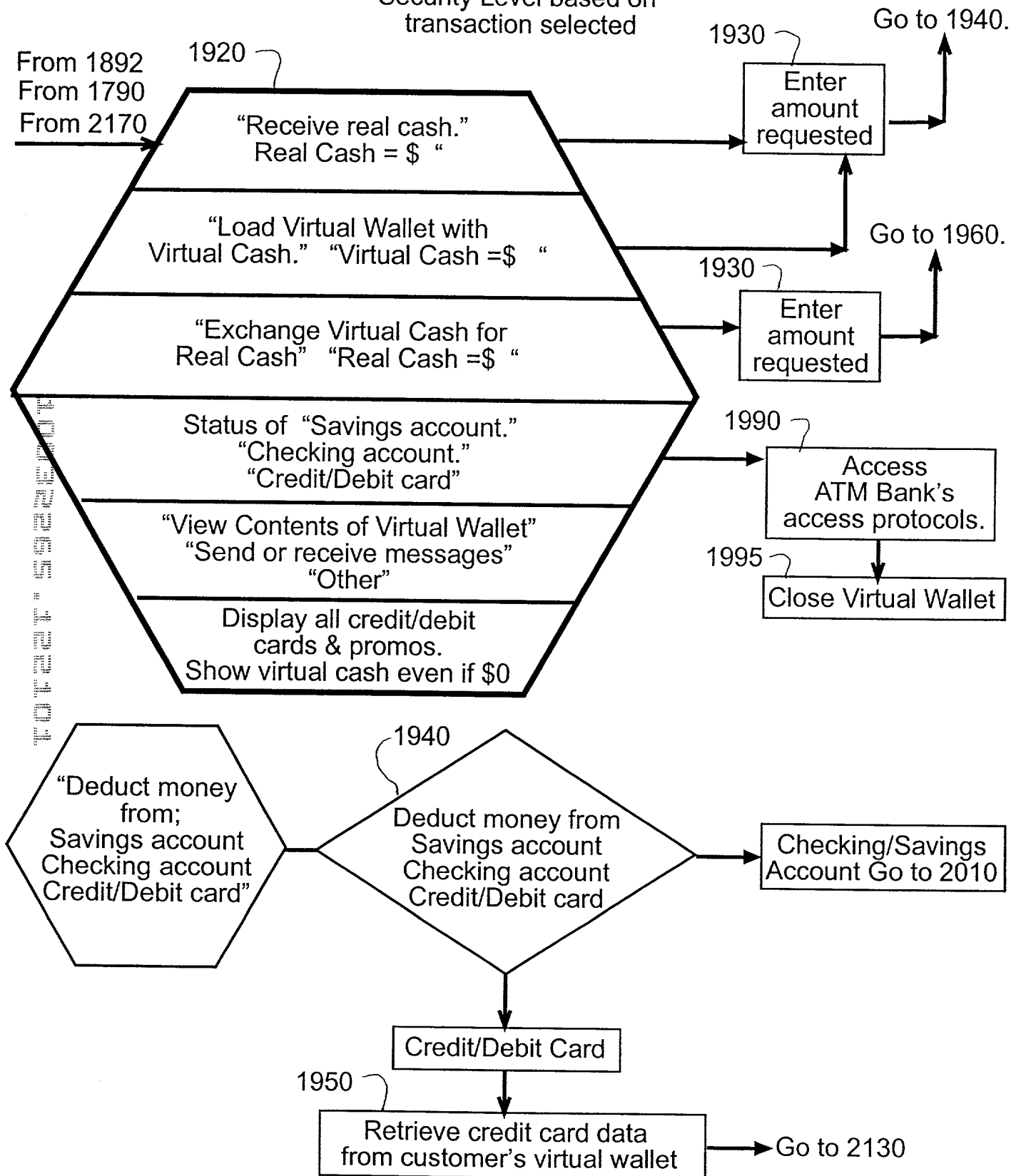


Figure 19

Security Level Logic Diagram

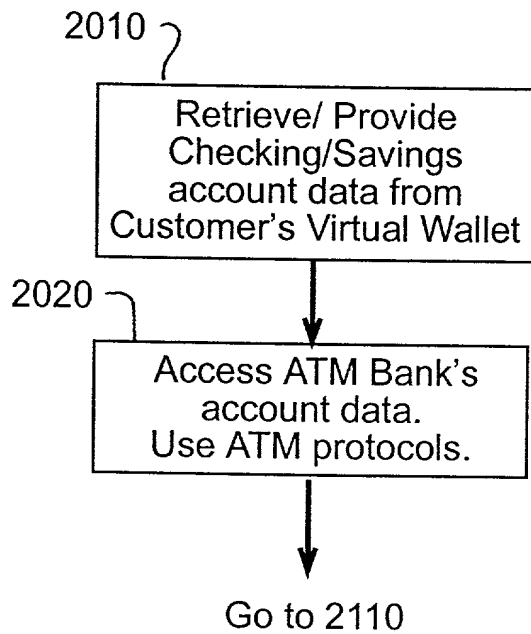


Figure 20

Security Level Logic Diagram

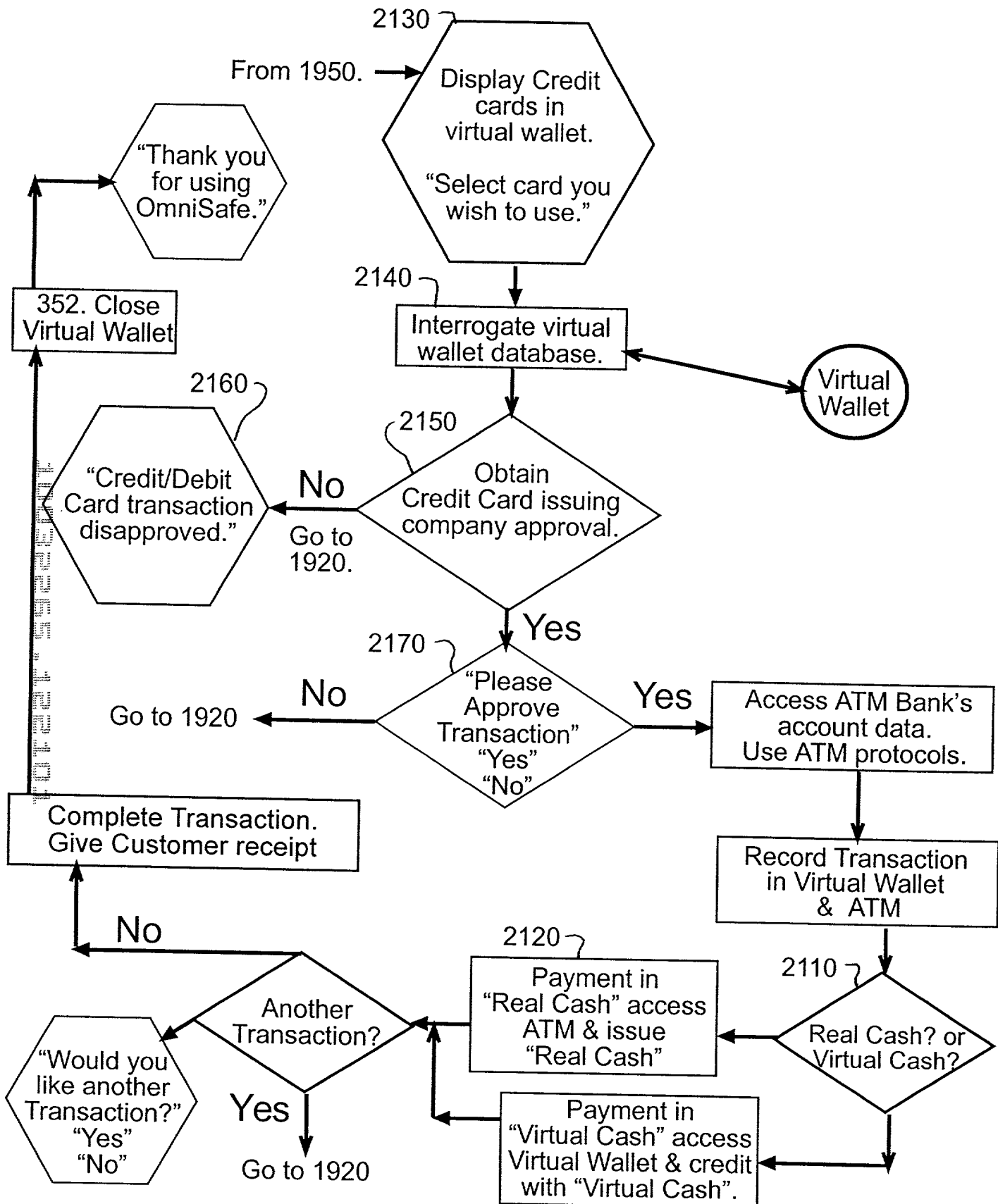


Figure 21

Security Level Logic Diagram

Exchange Cash Transaction

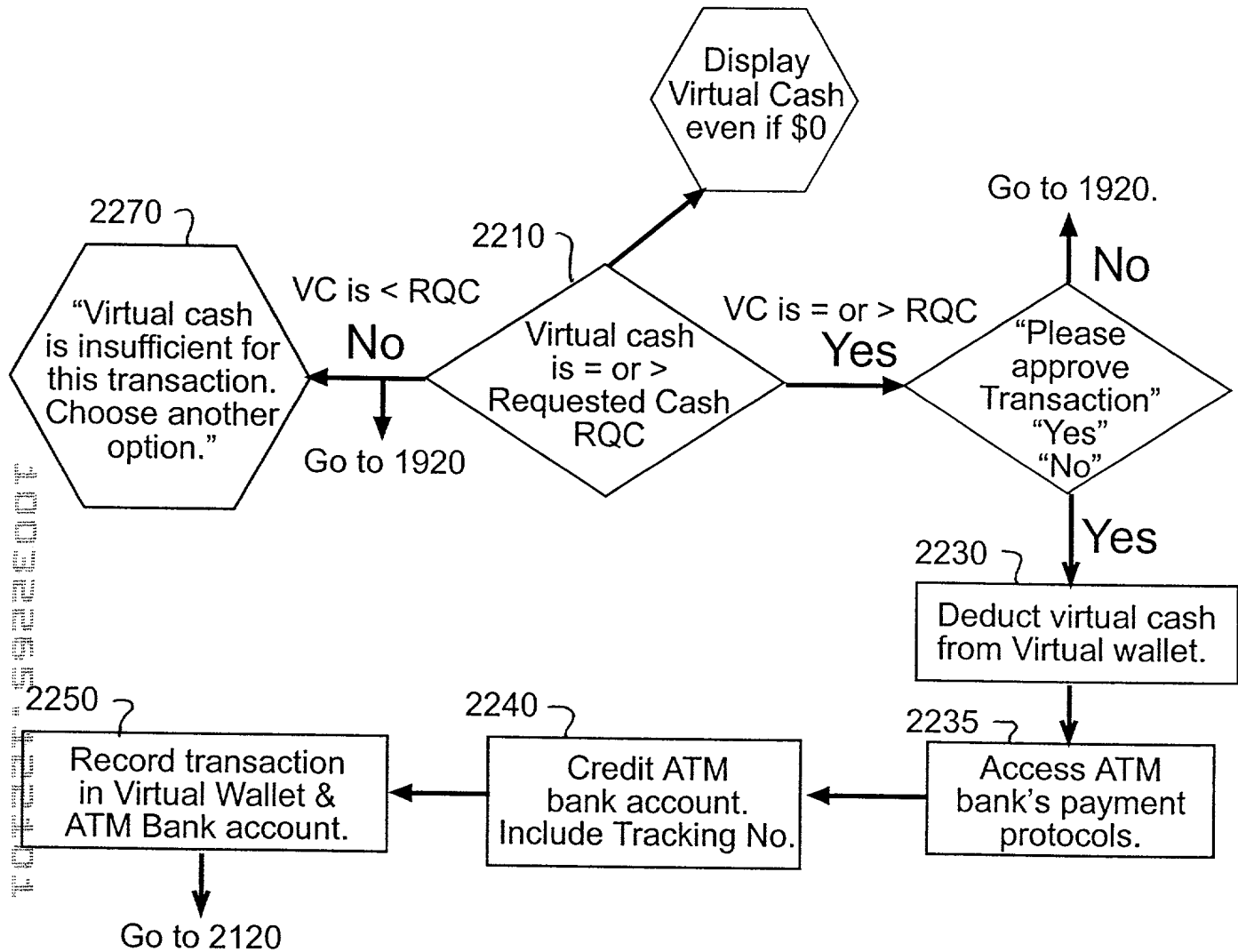


Figure 22

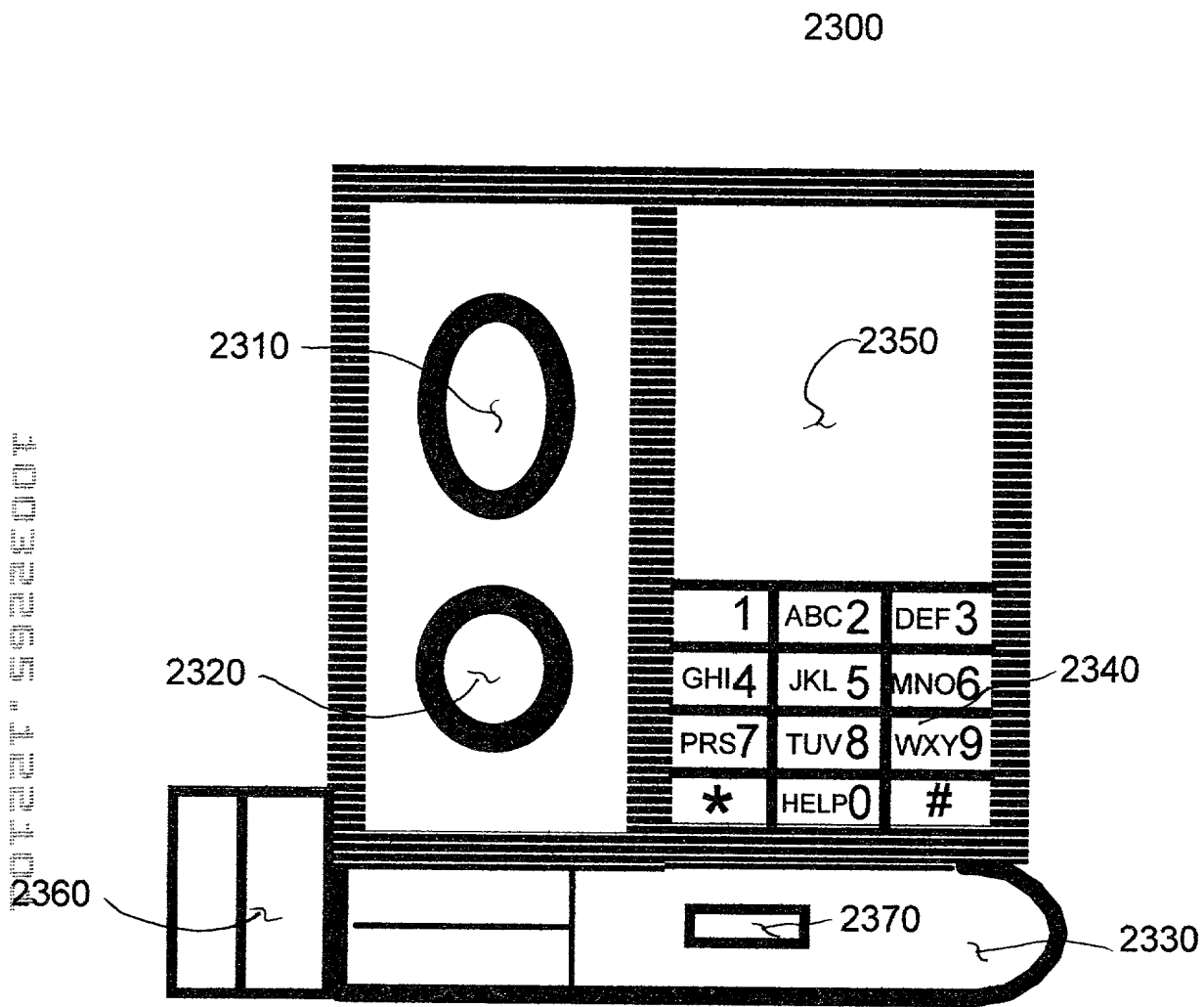


Figure 23

High Memory Virtual Wallet

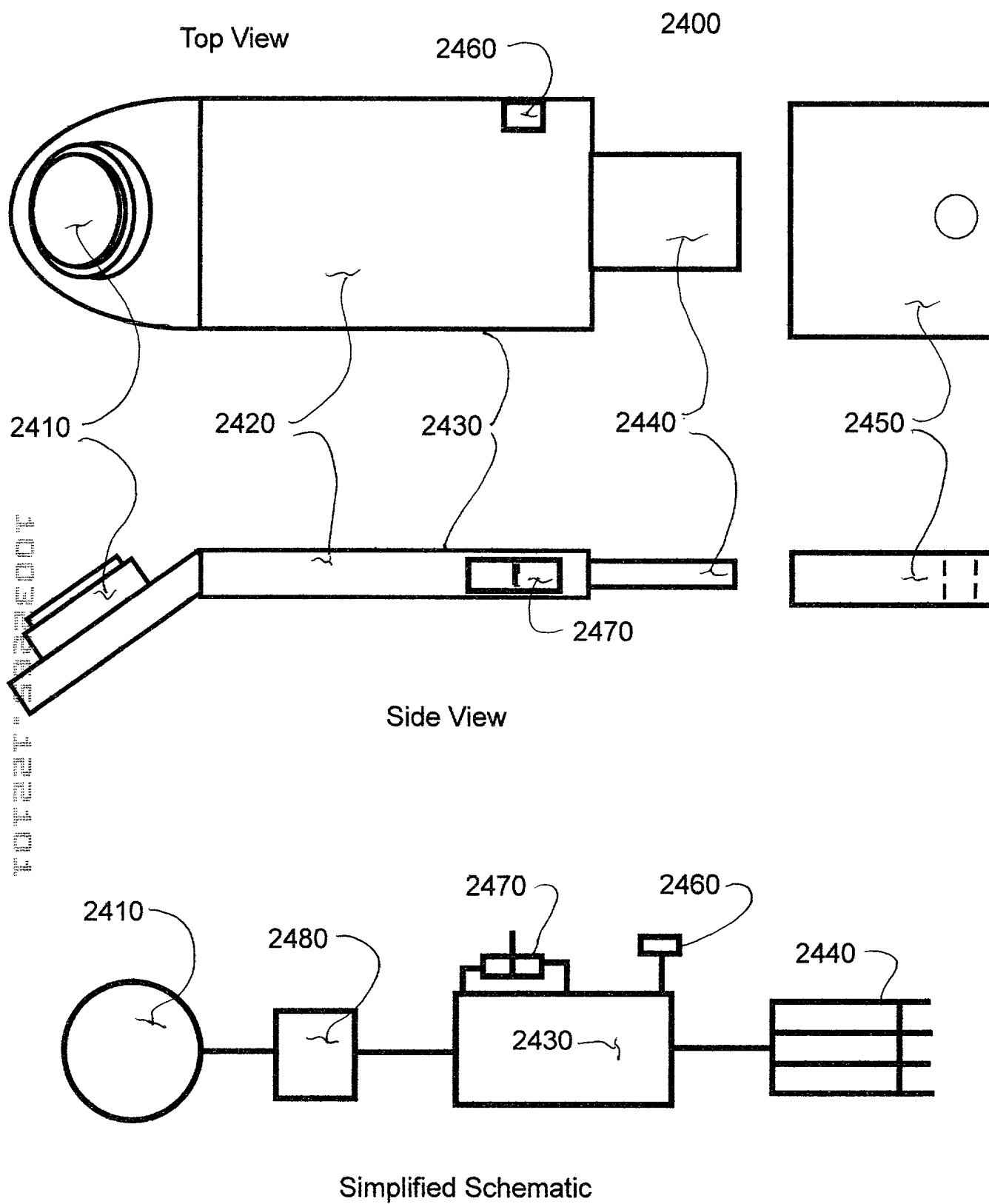
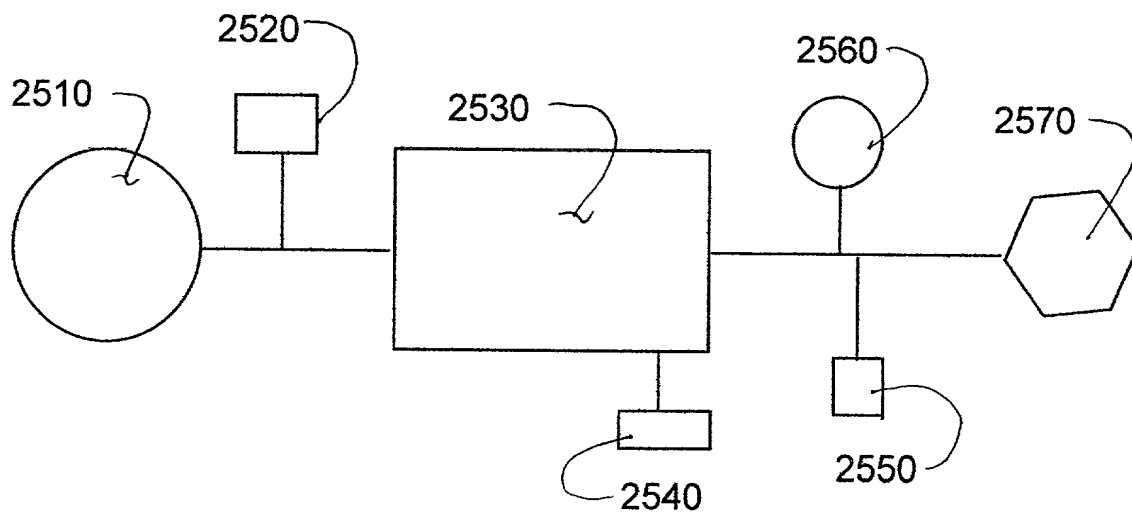
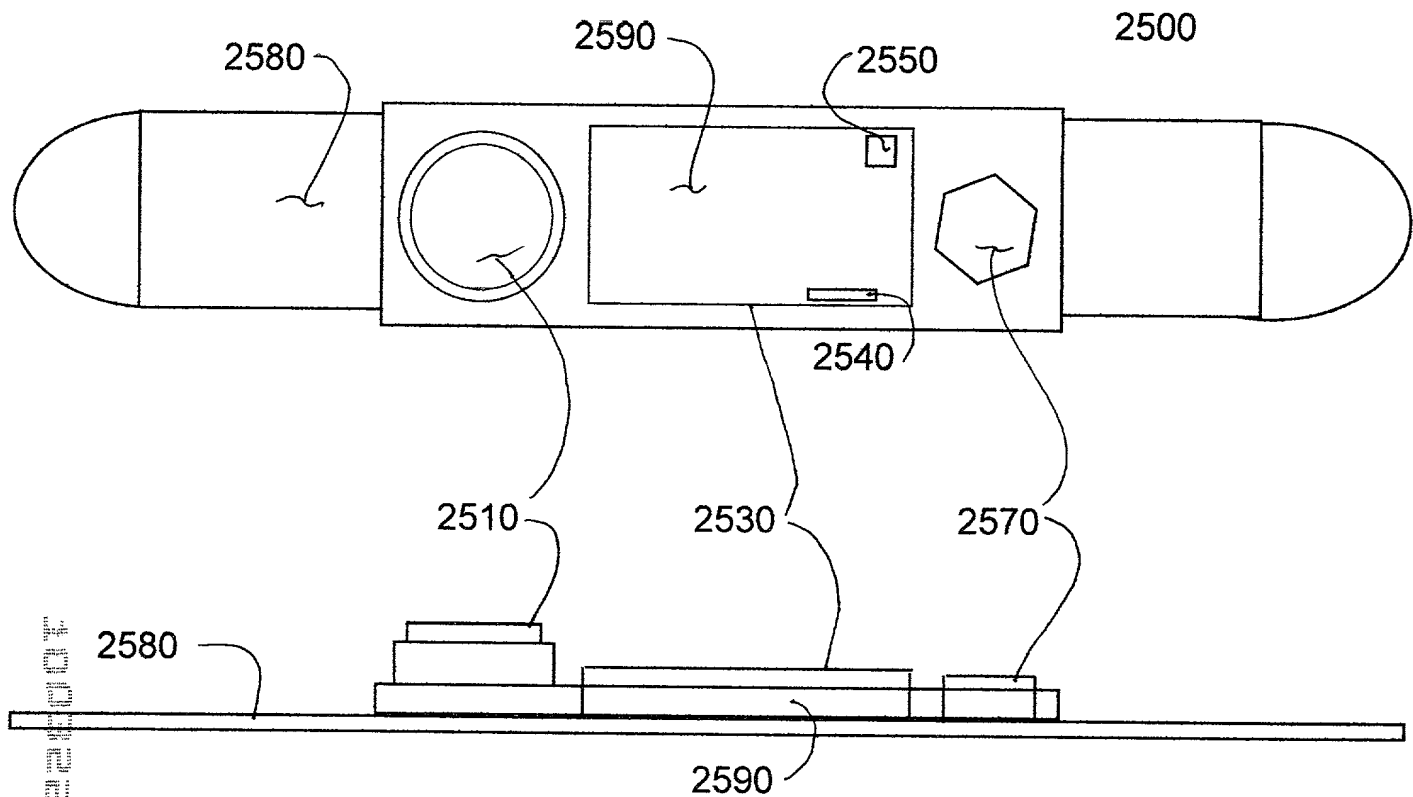


Figure 24

Data Wrist Rocket



Simplified Schematic

Figure 25

Touch Interface Access Wand

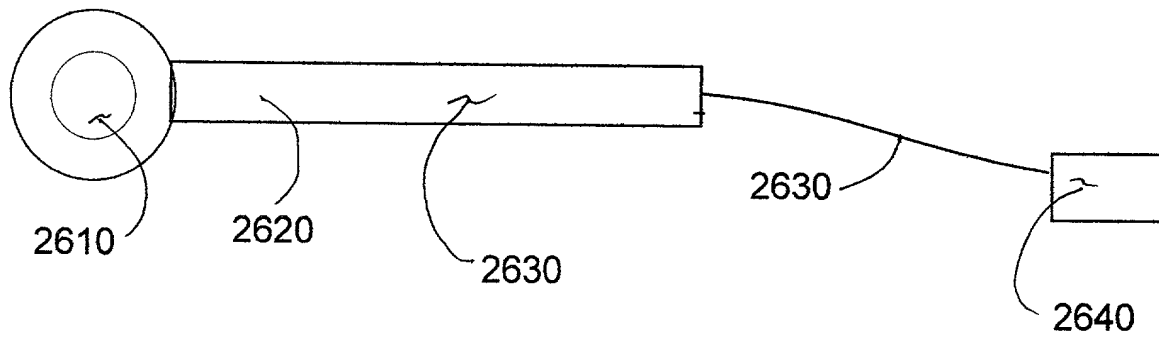


Figure 26

Security Level Logic Diagram

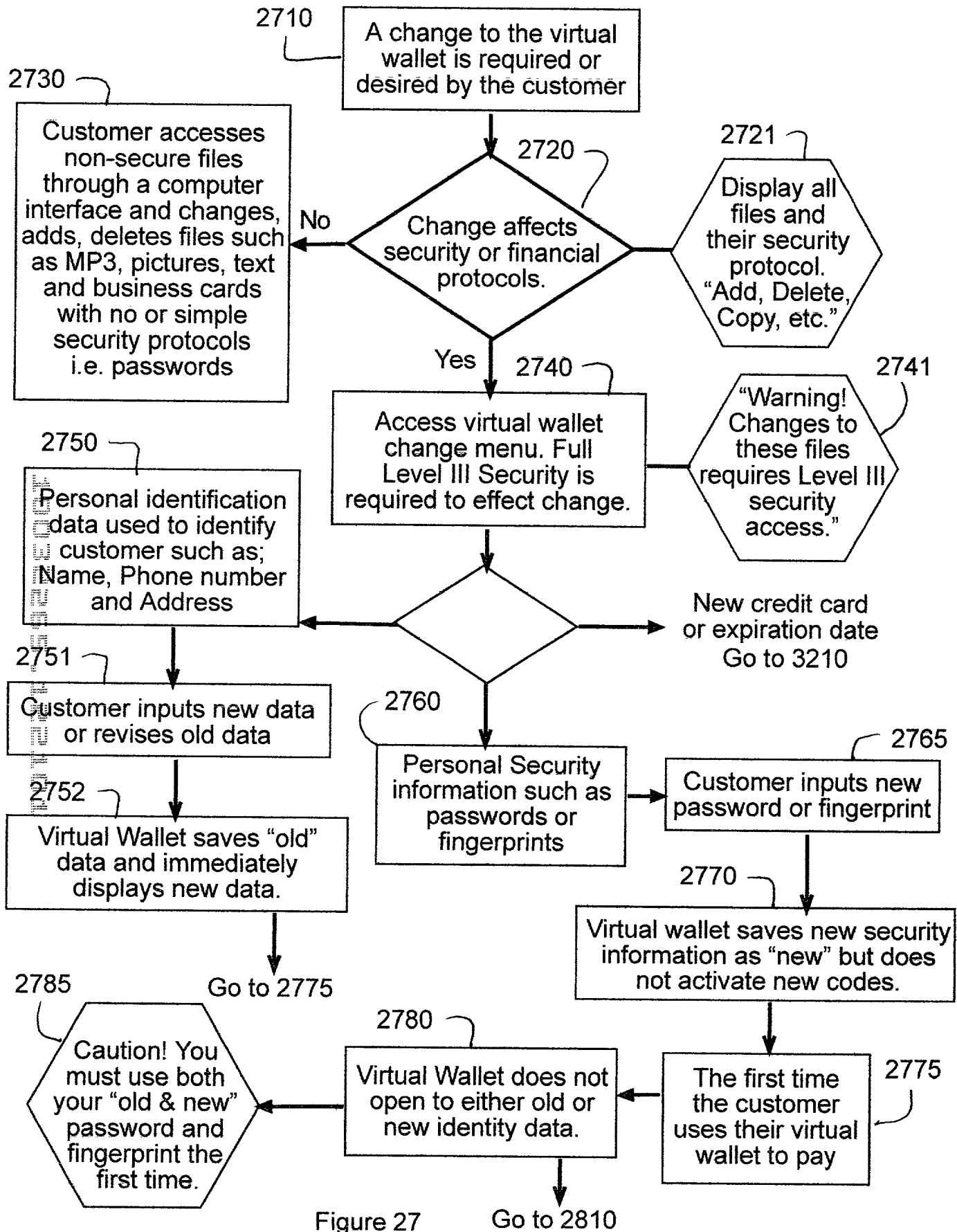


Figure 27

Go to 2810

Security Level Logic Diagram

Confirm Old Data/Security Protocols

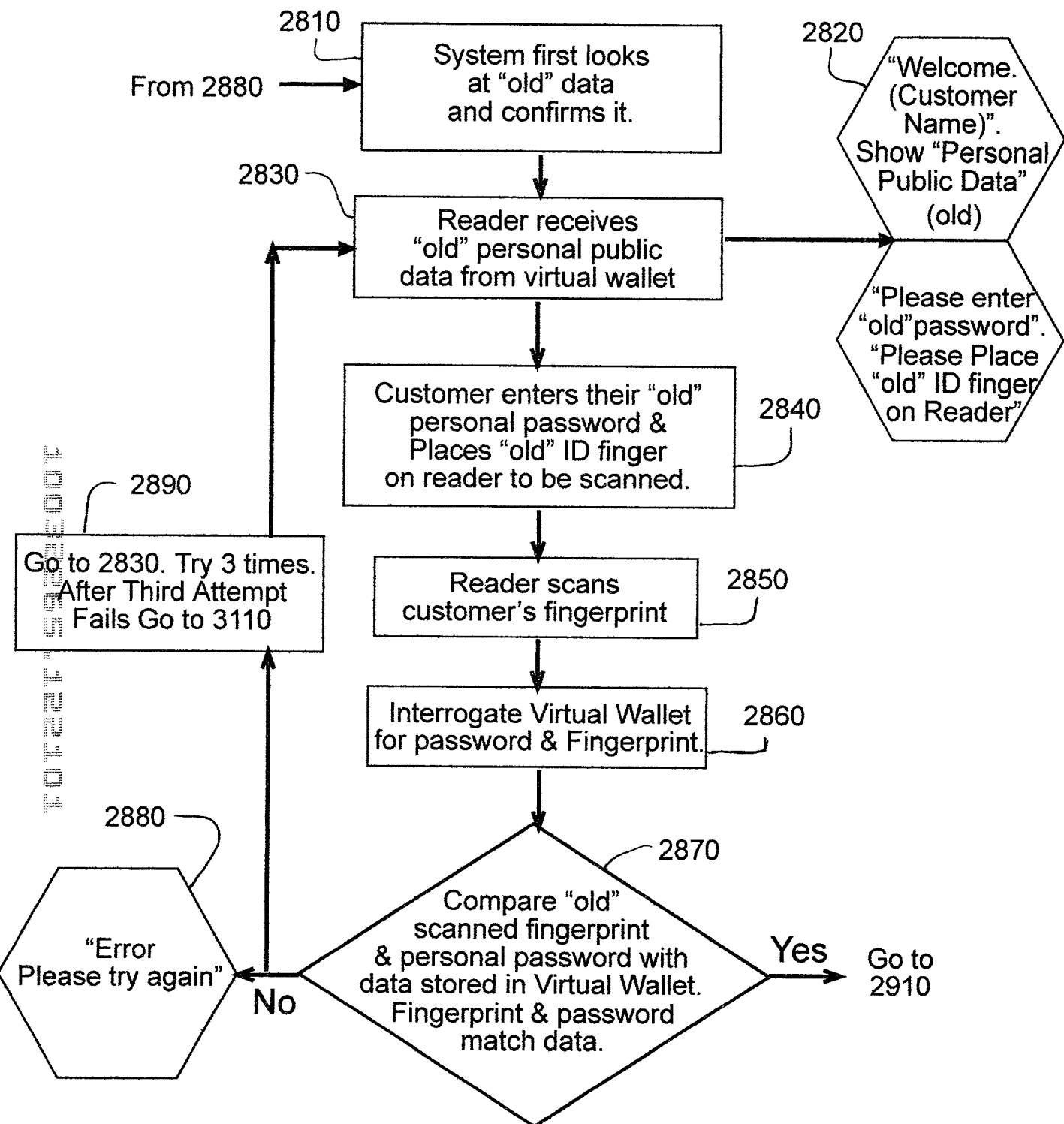


Figure 28

Security Level Logic Diagram

Confirm New Data/Security Protocols

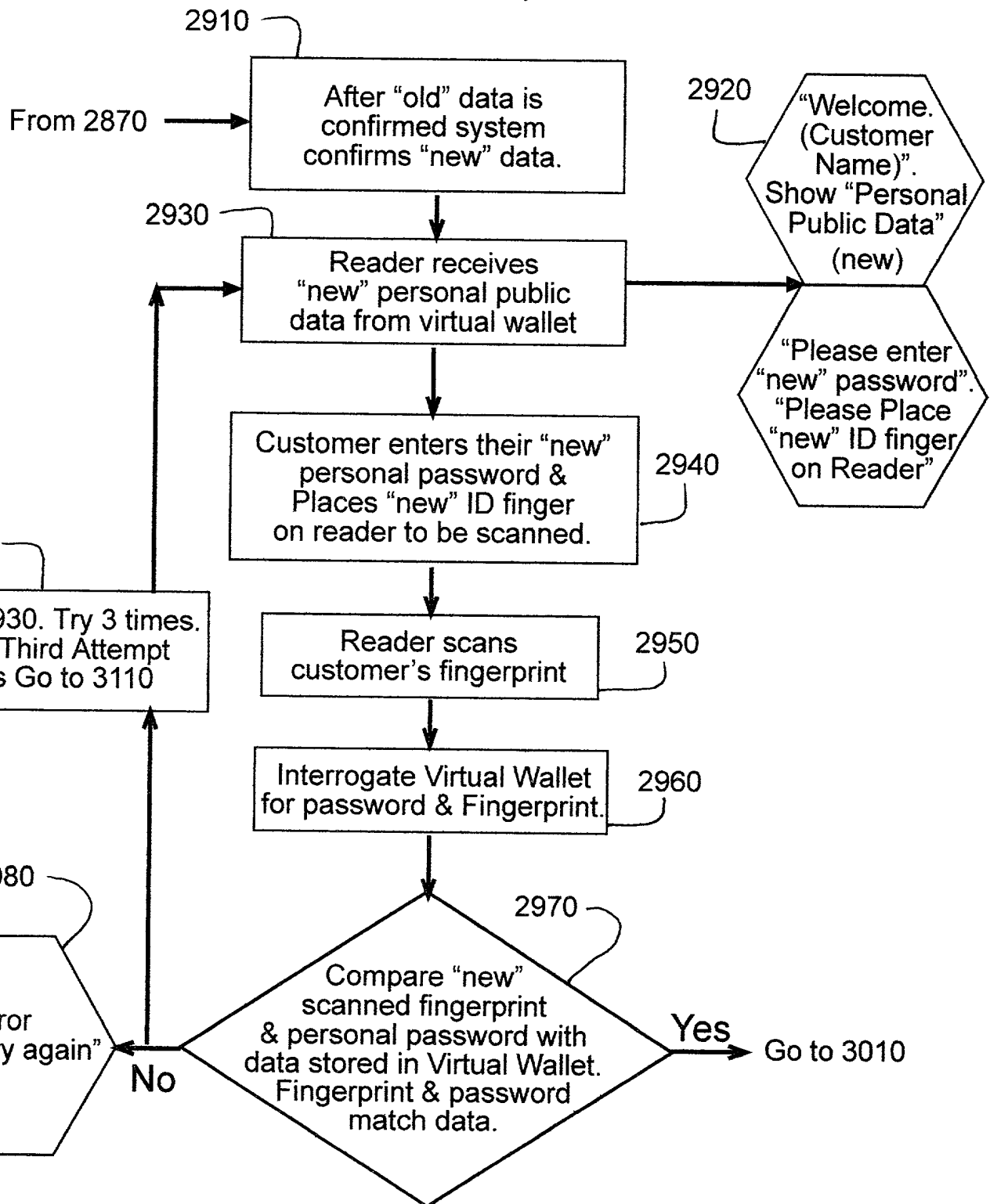


Figure 29

Security Level Logic Diagram

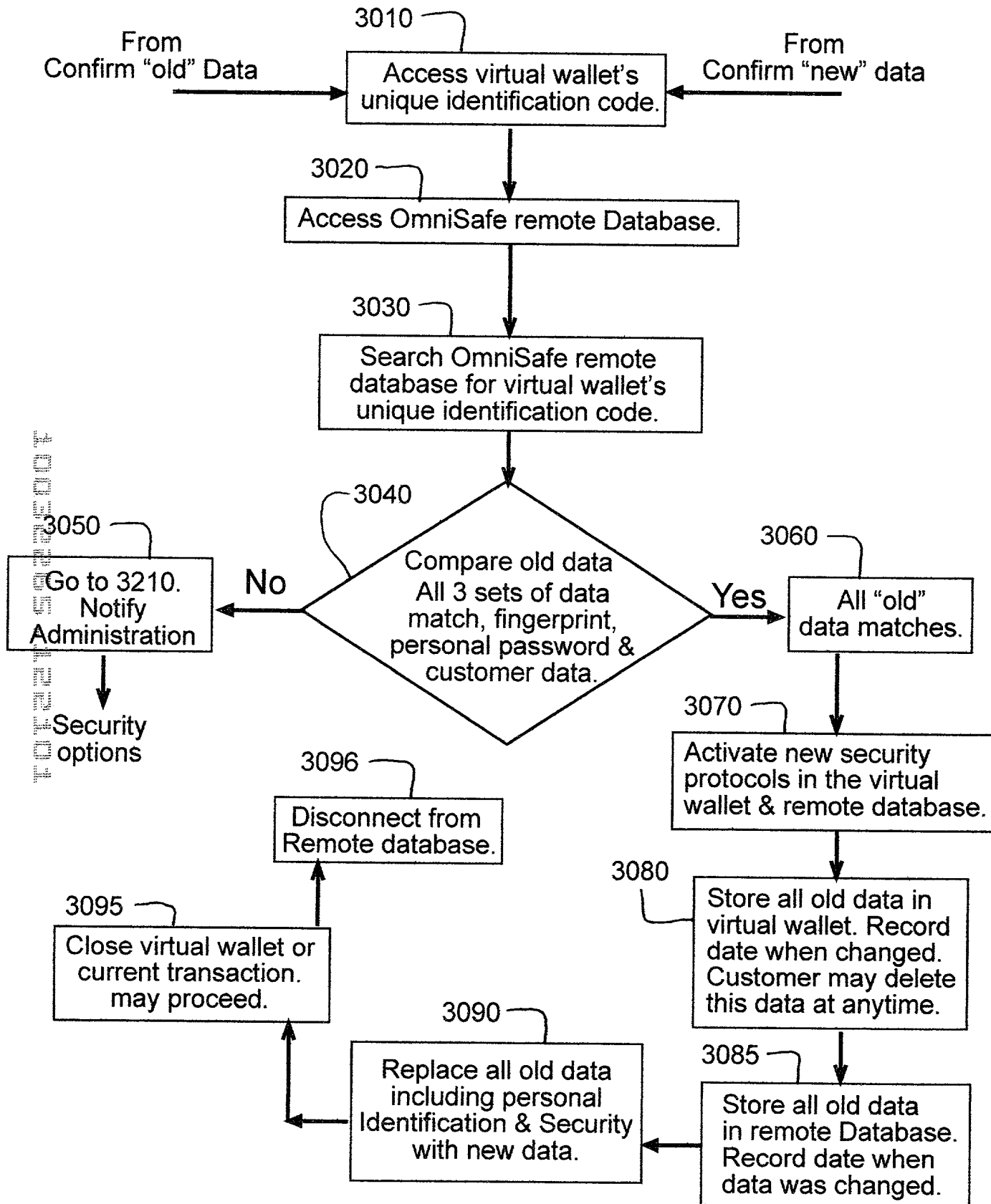


Figure 30

Security Level Logic Diagram

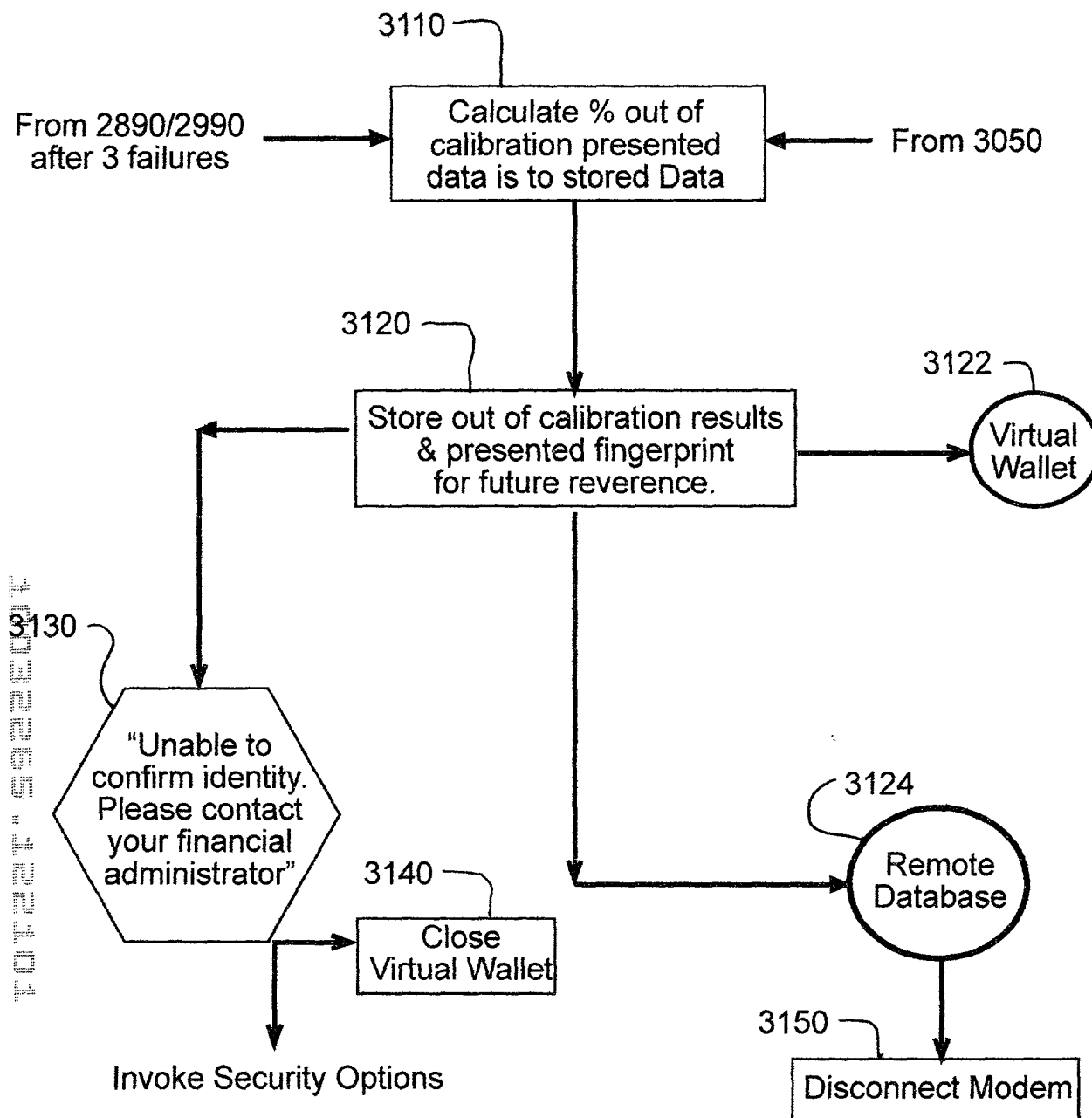


Figure 31

Security Level Logic Diagram

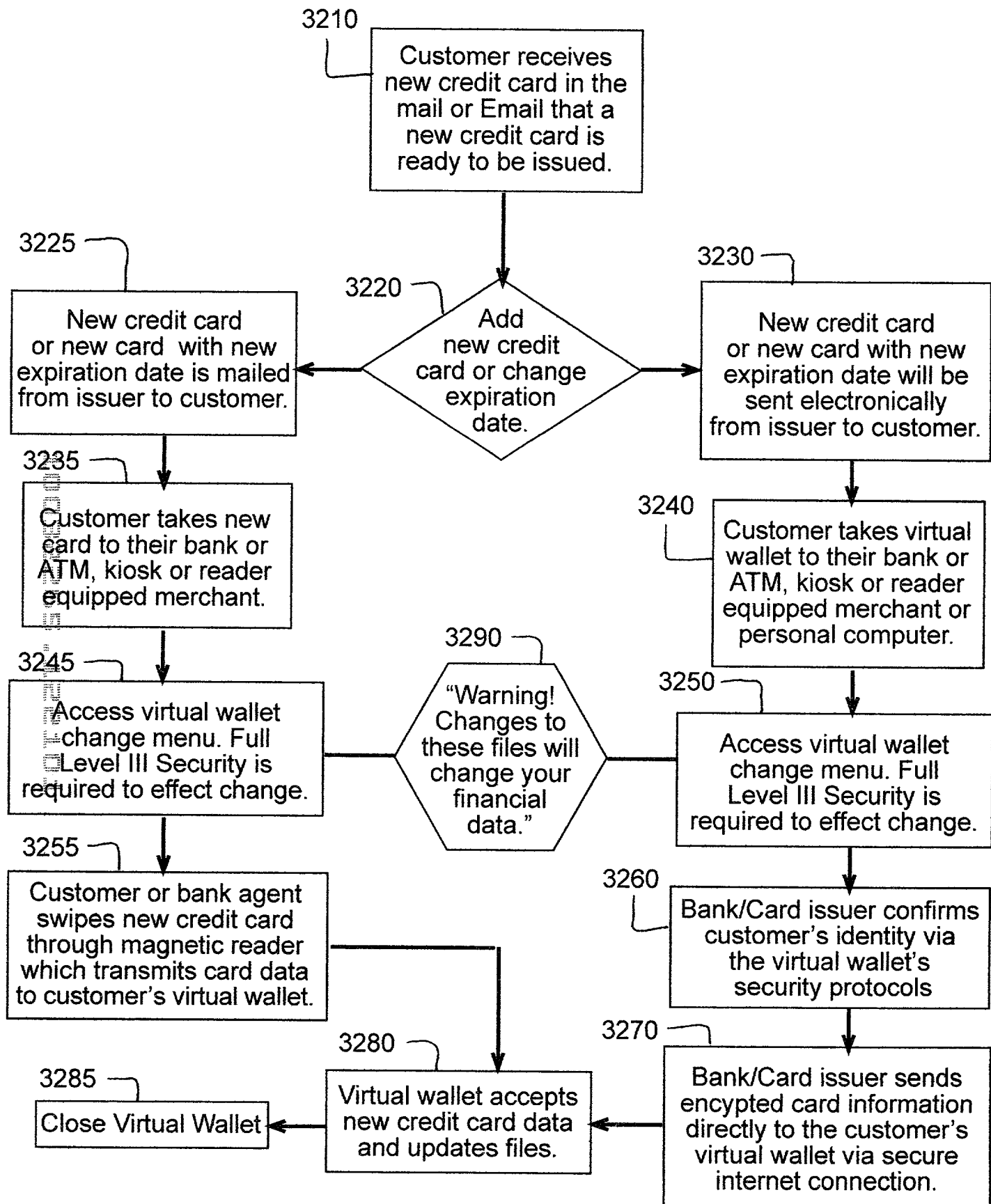


Figure 32

Security Level Logic Diagram

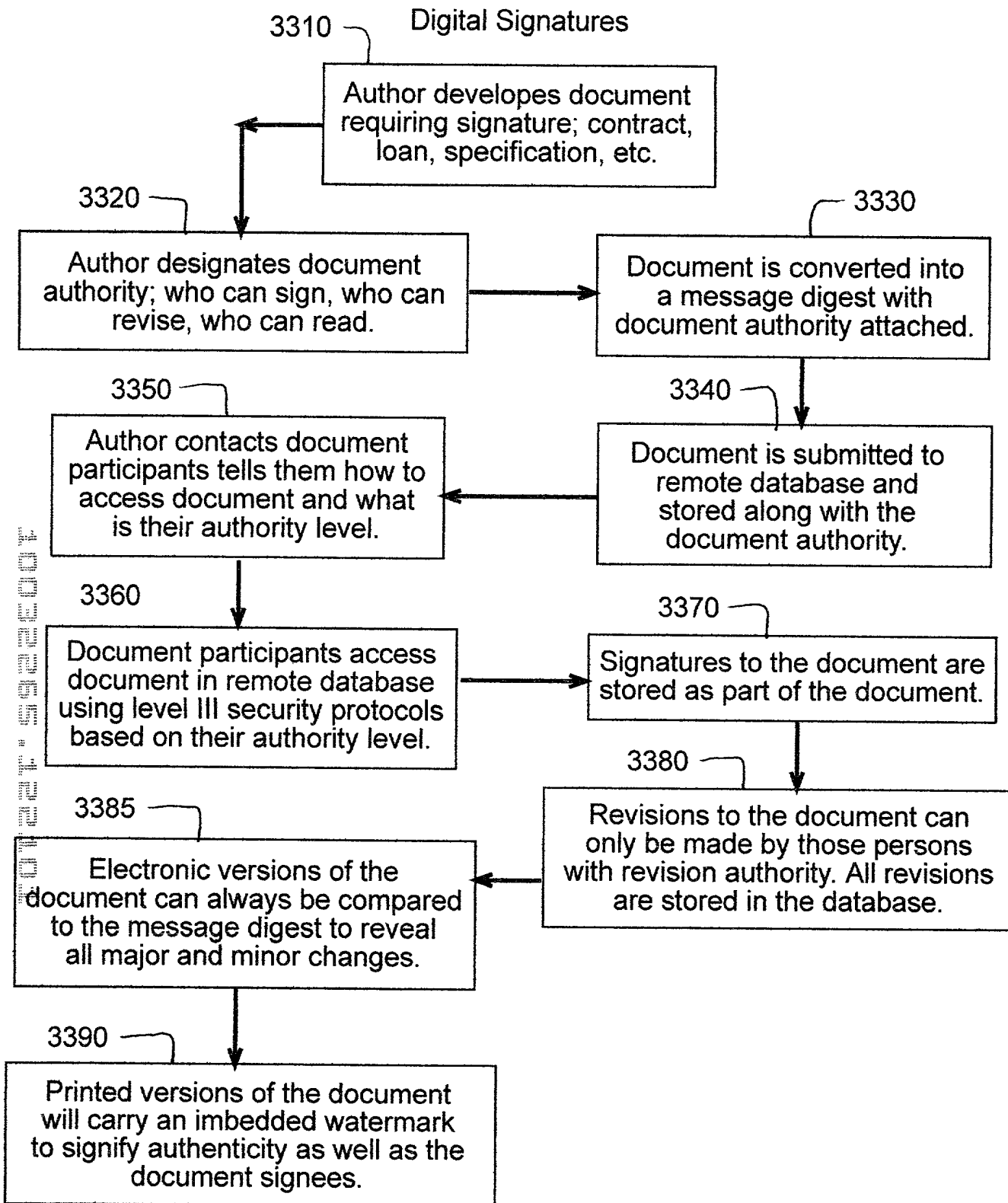


Figure 33

Security Level Logic Diagram

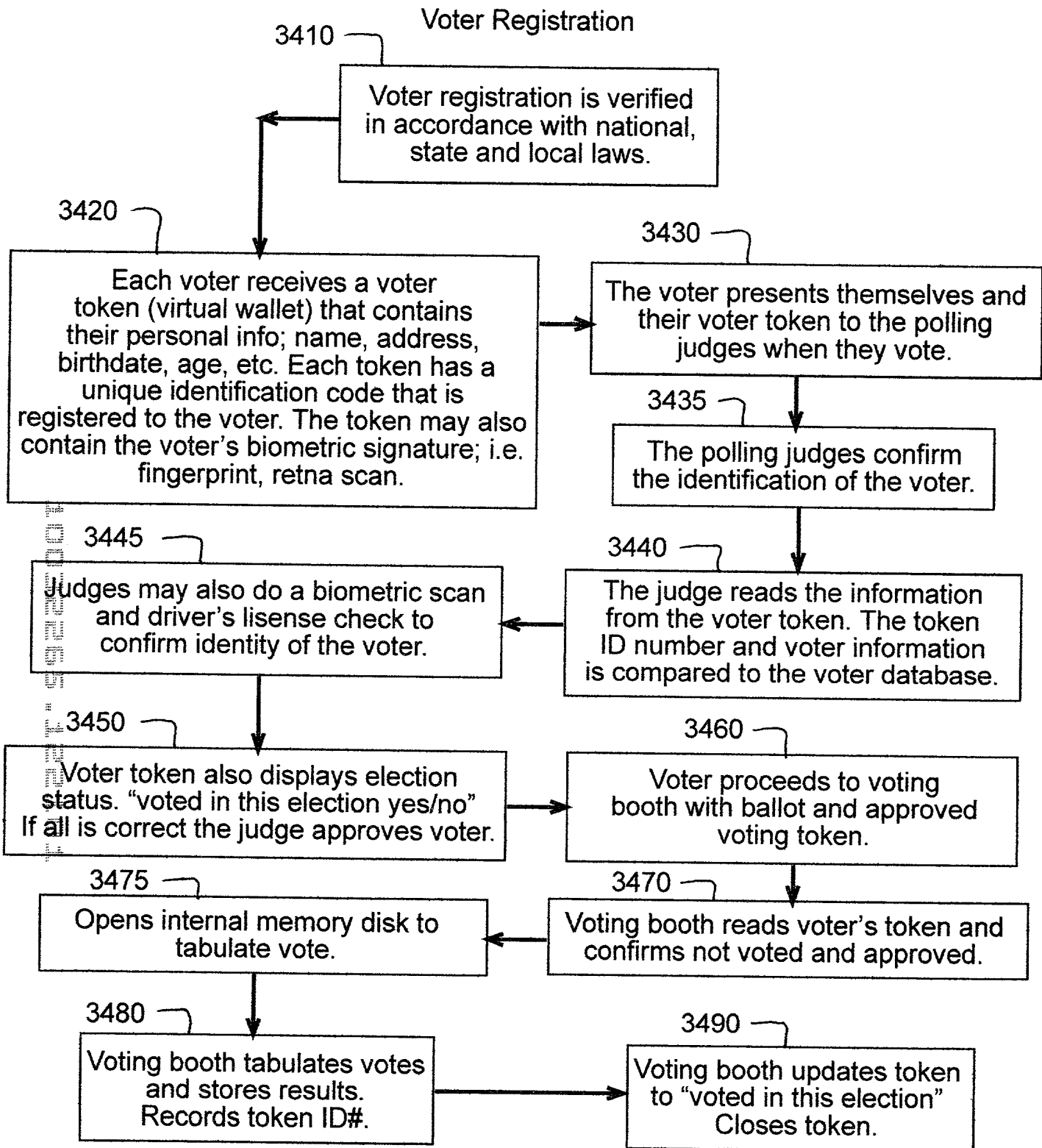


Figure 34

High Memory Virtual Wallet

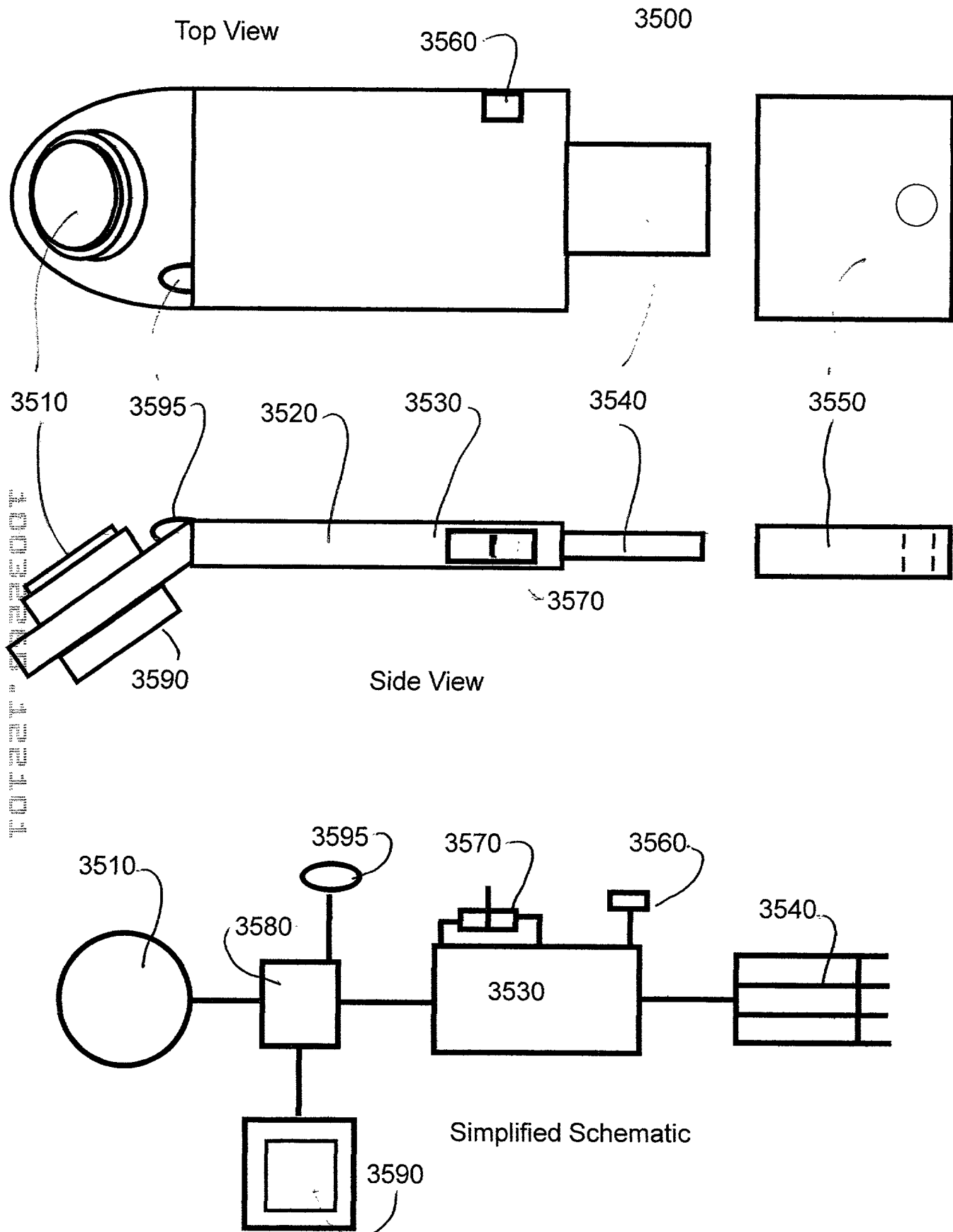
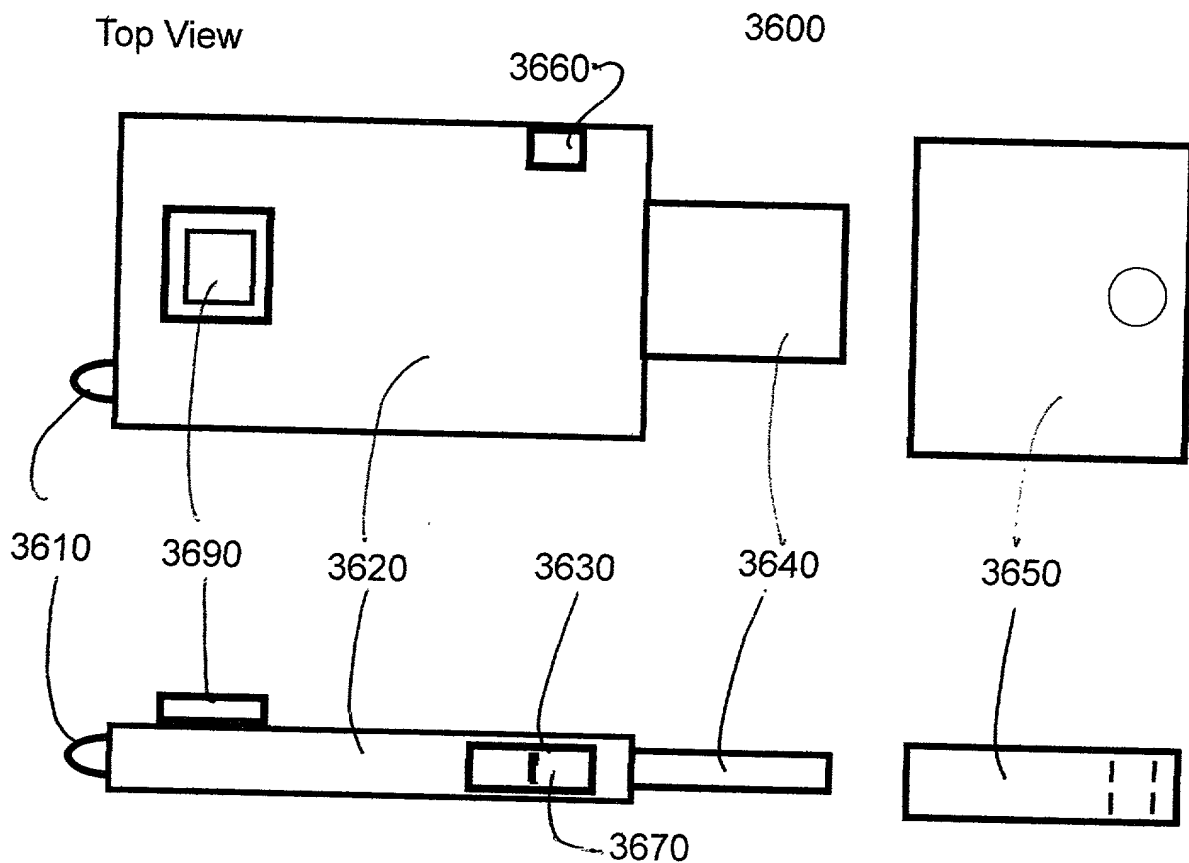


Figure 35

High Memory Virtual Wallet with Transmitter



Side View

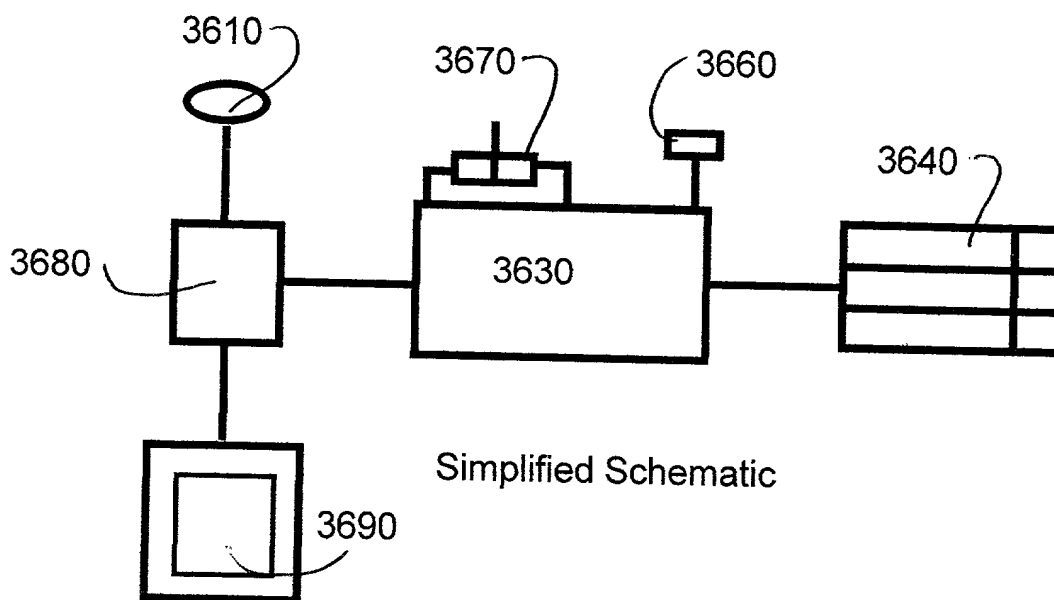


Figure 36

General Data Flow and Processing

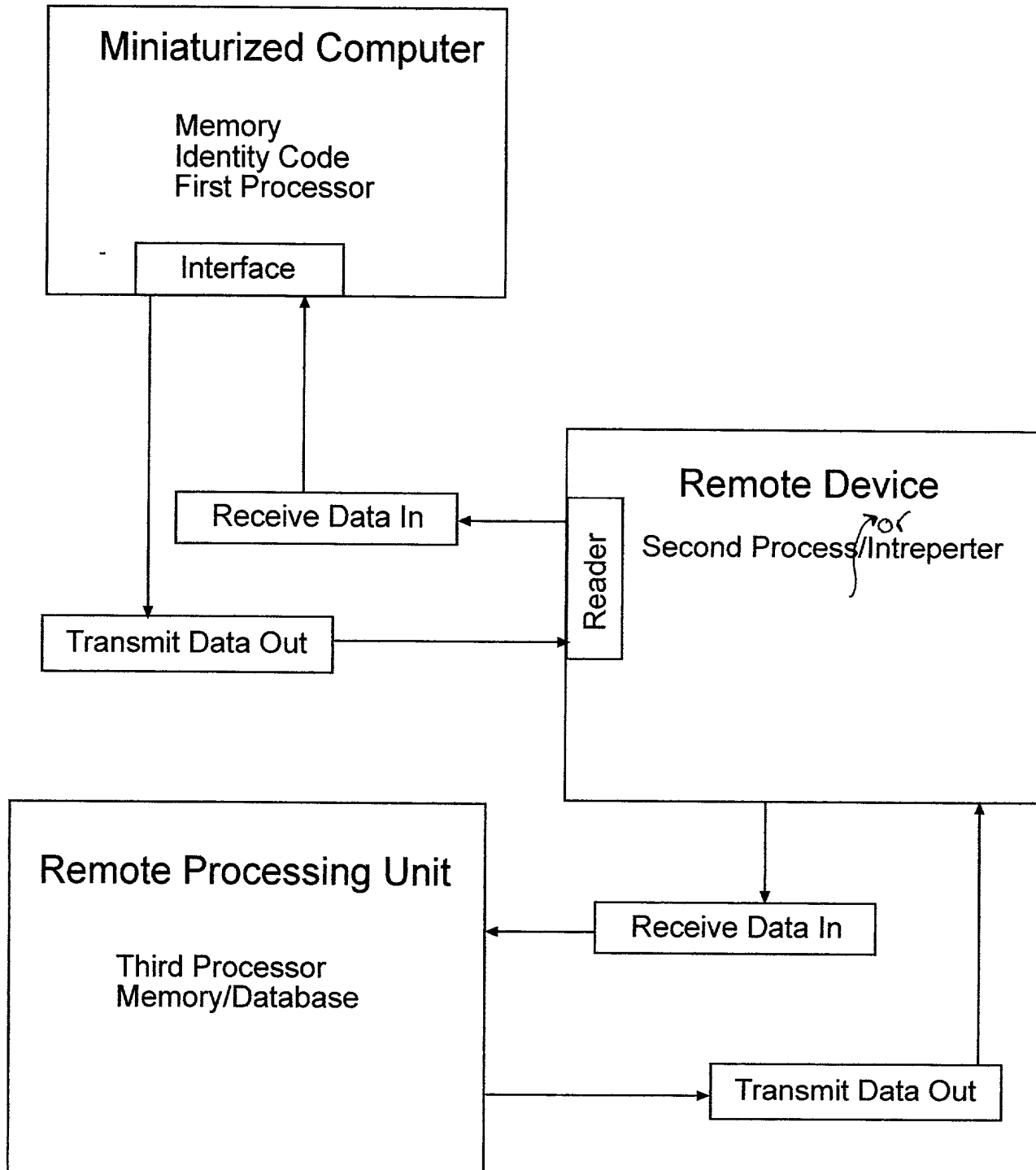


Figure 37